

FIG. 1

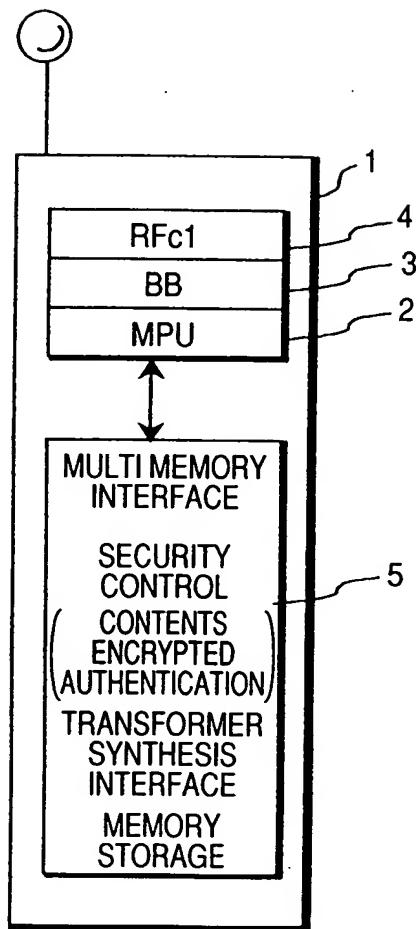


FIG. 2

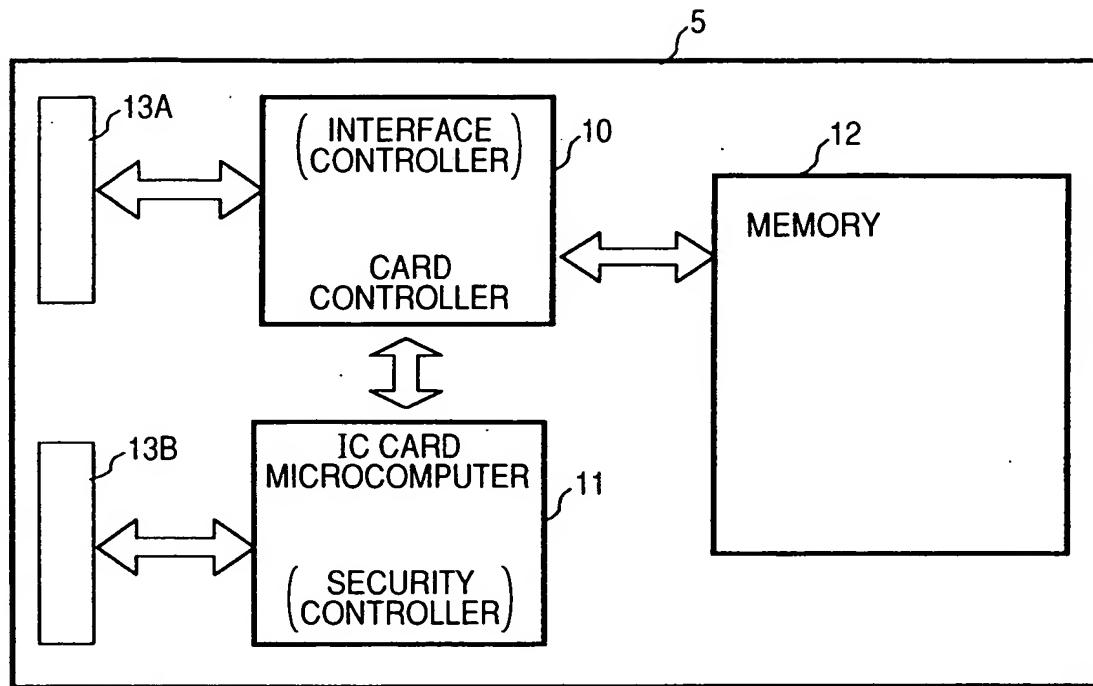


FIG. 3

A pin assignment table for a card component, showing connections between pins and their corresponding functions. The table is organized into two columns. The left column lists pins 1 through 10, and the right column lists pins 11 through 20. A vertical line labeled '5' is positioned to the right of the table.

10	LA	LB	20
9	I/O-ic	CLK-ic	19
8	Vss	RES-ic	18
7	DAT1	Vcc-ic	17
6	DAT0	BS-ms	16
5	CLK/SCLK-ms	DAT7/D1-ms	15
4	Vcc	DAT6/SDIO/D0-ms	14
3	CMD	DAT5/D2-ms	13
2	CD/DAT3	INS-ms	12
1	DAT2	DAT4/D3-ms	11

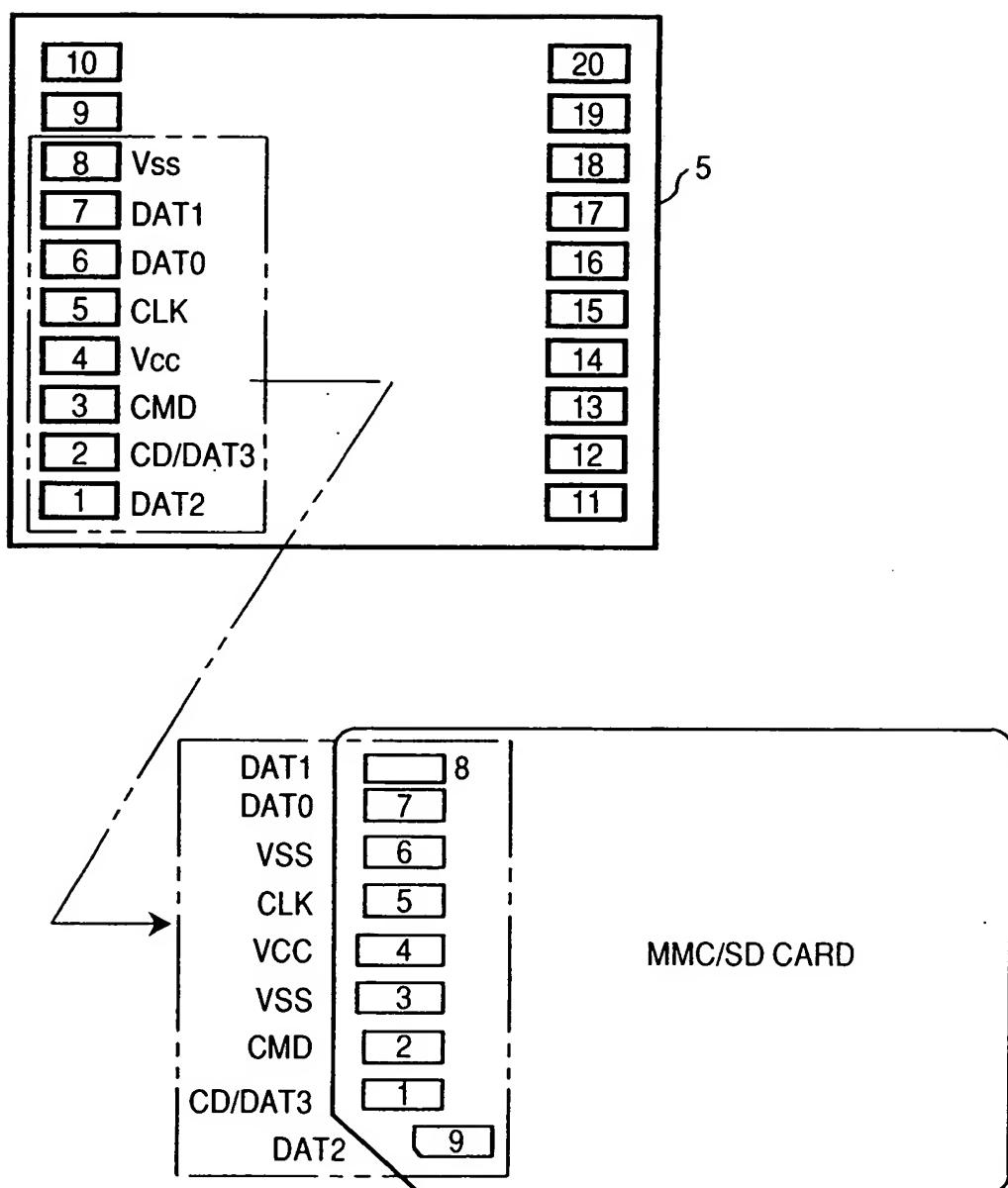
FIG. 4

FIG. 5

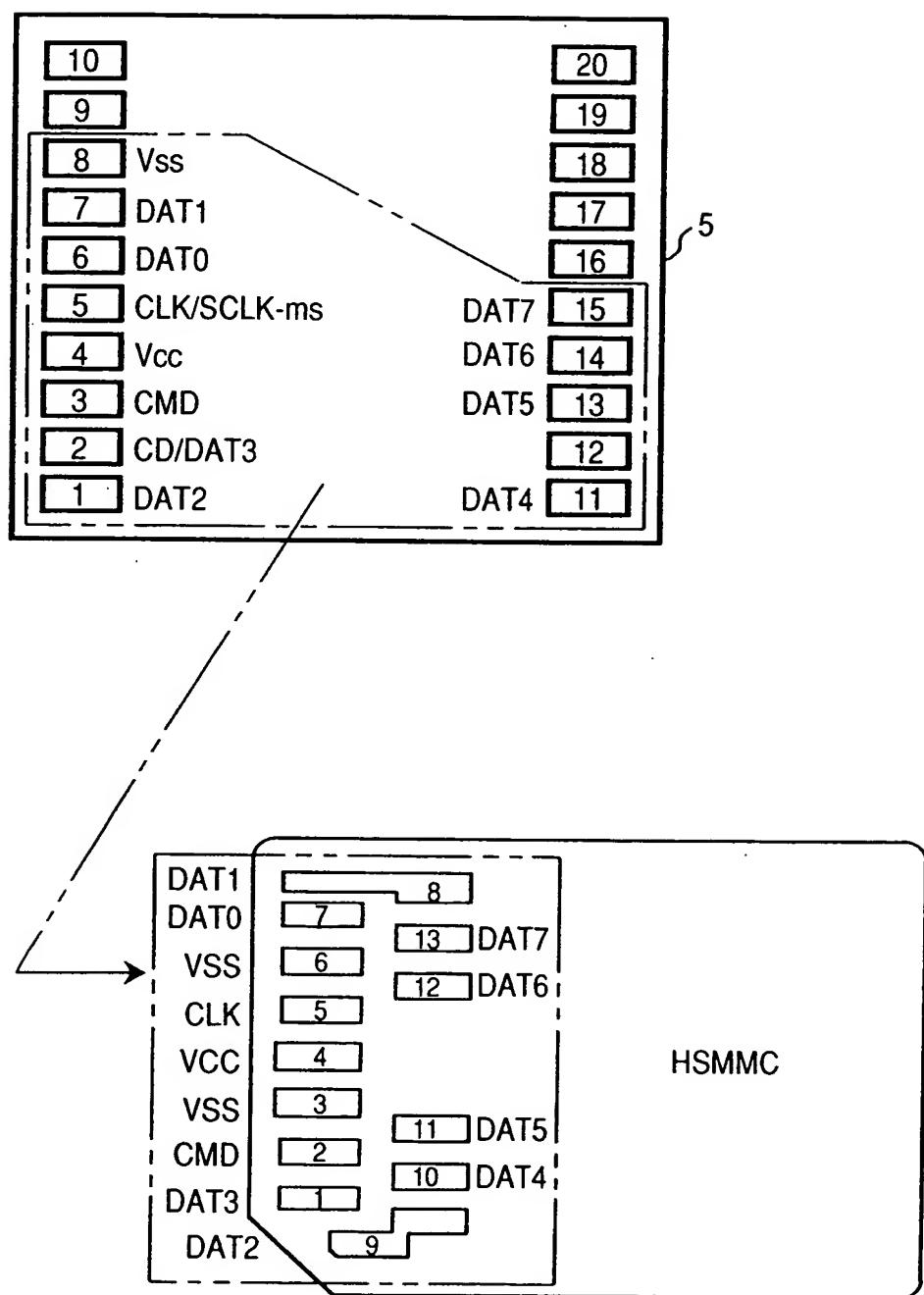


FIG. 6

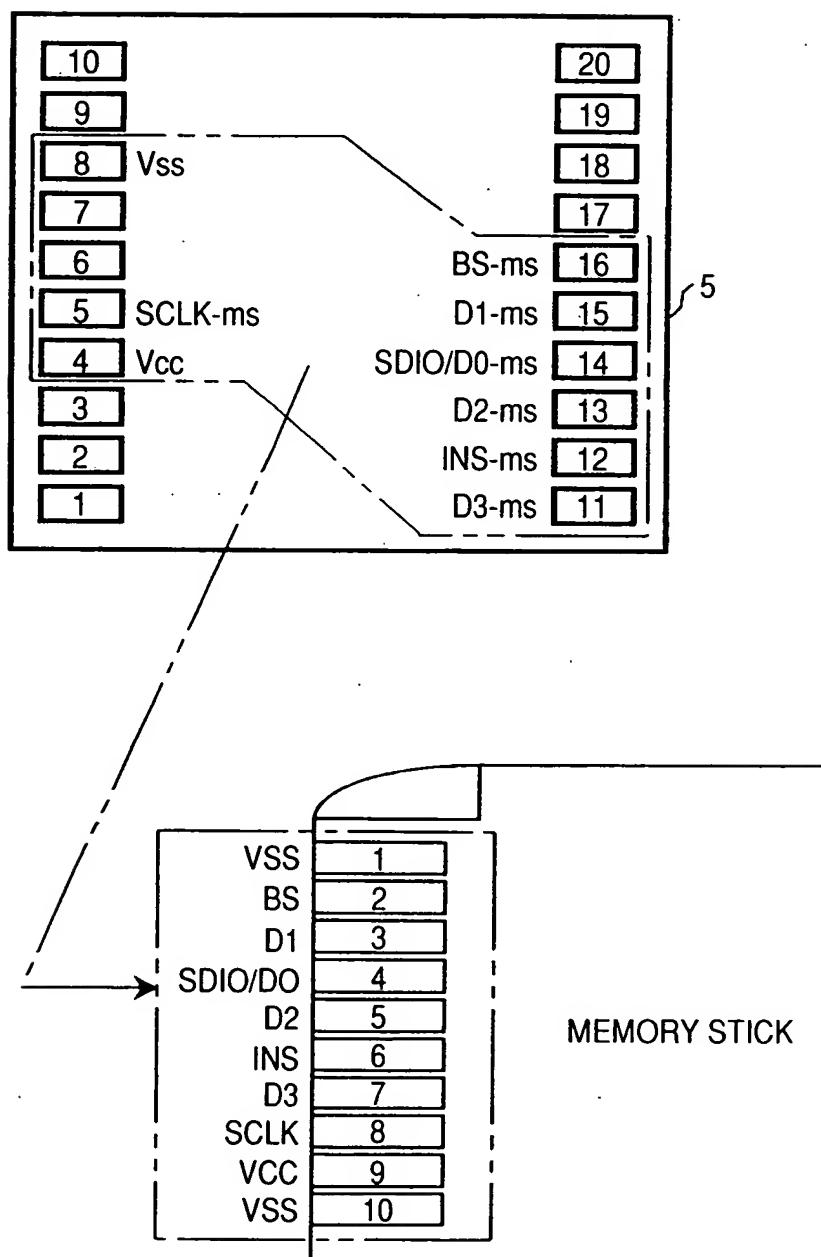


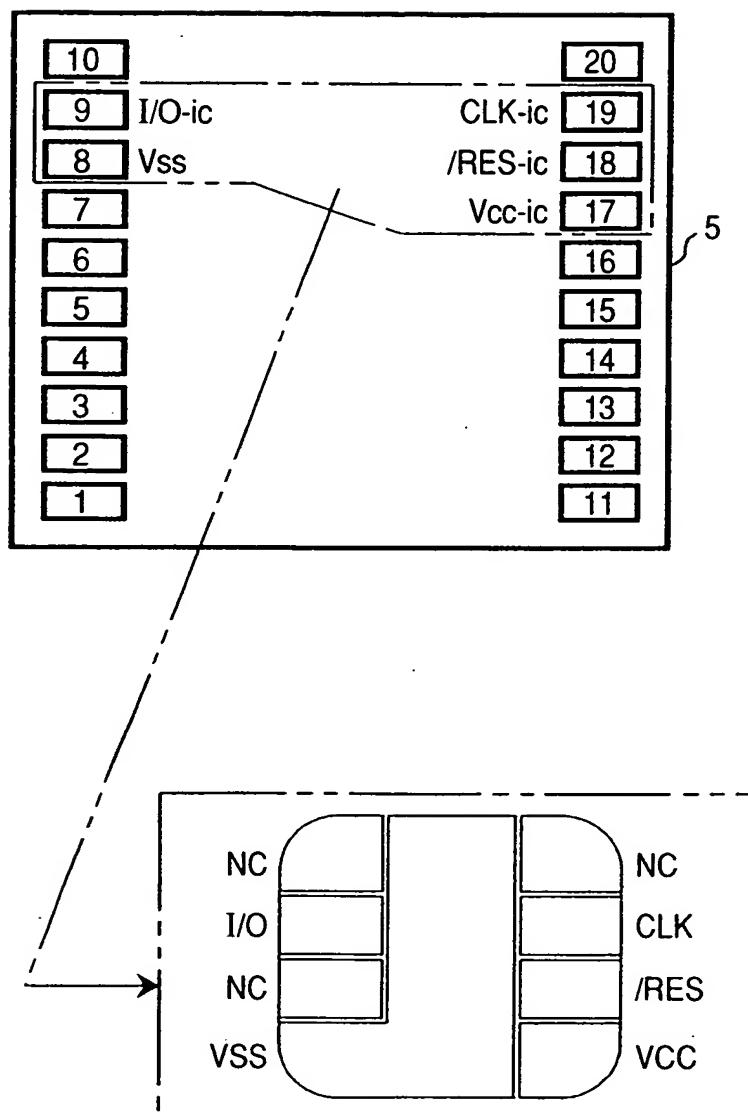
FIG. 7

FIG. 8

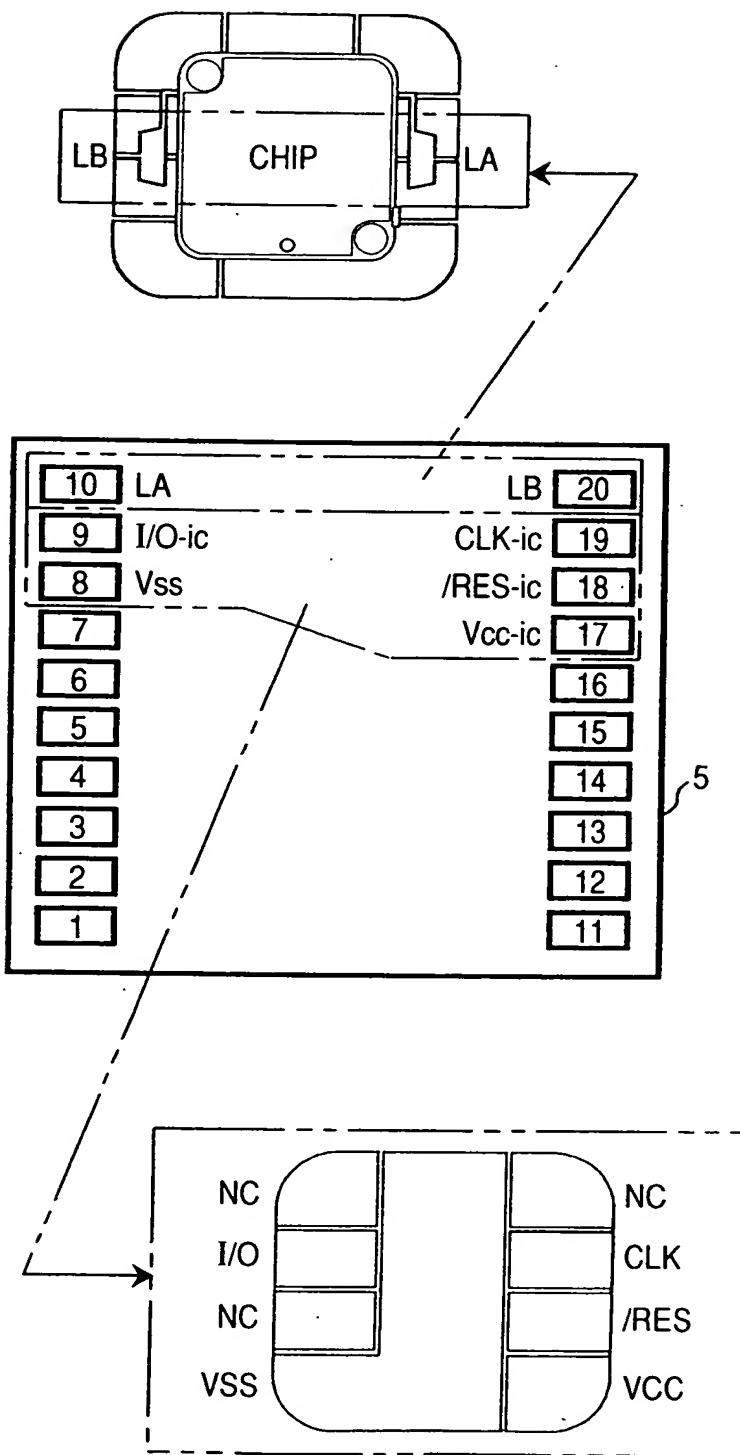


FIG. 9

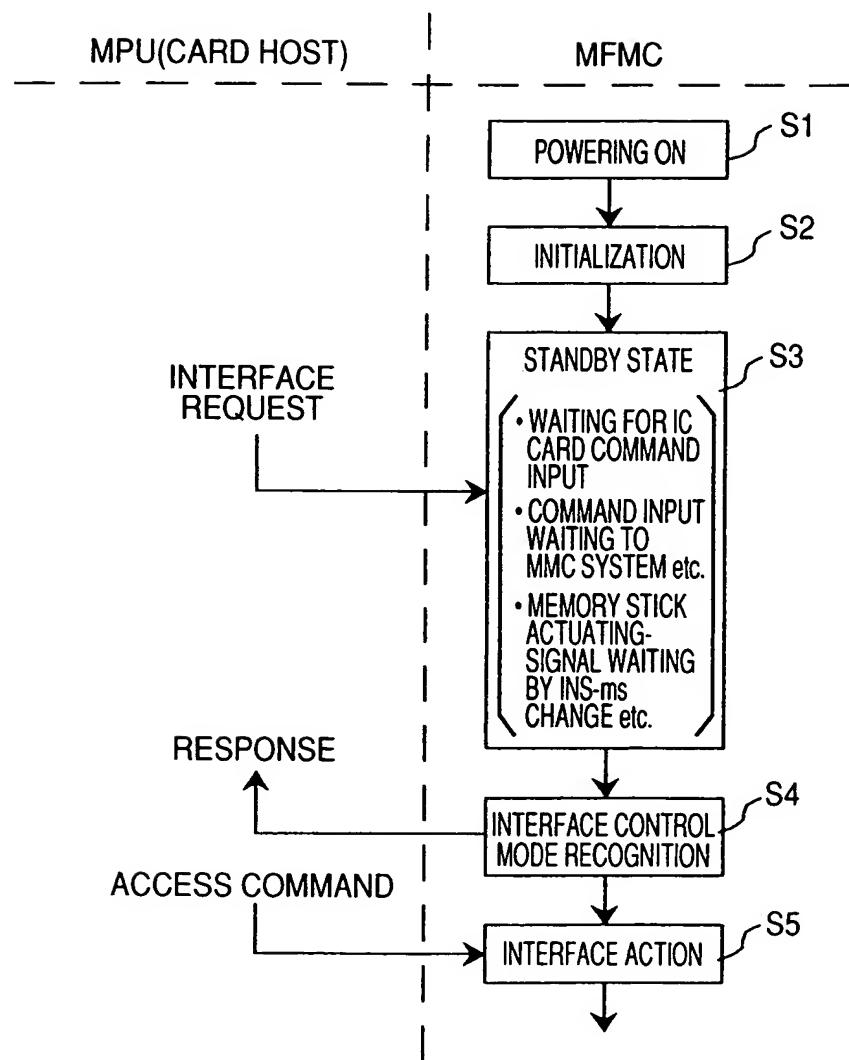


FIG. 10

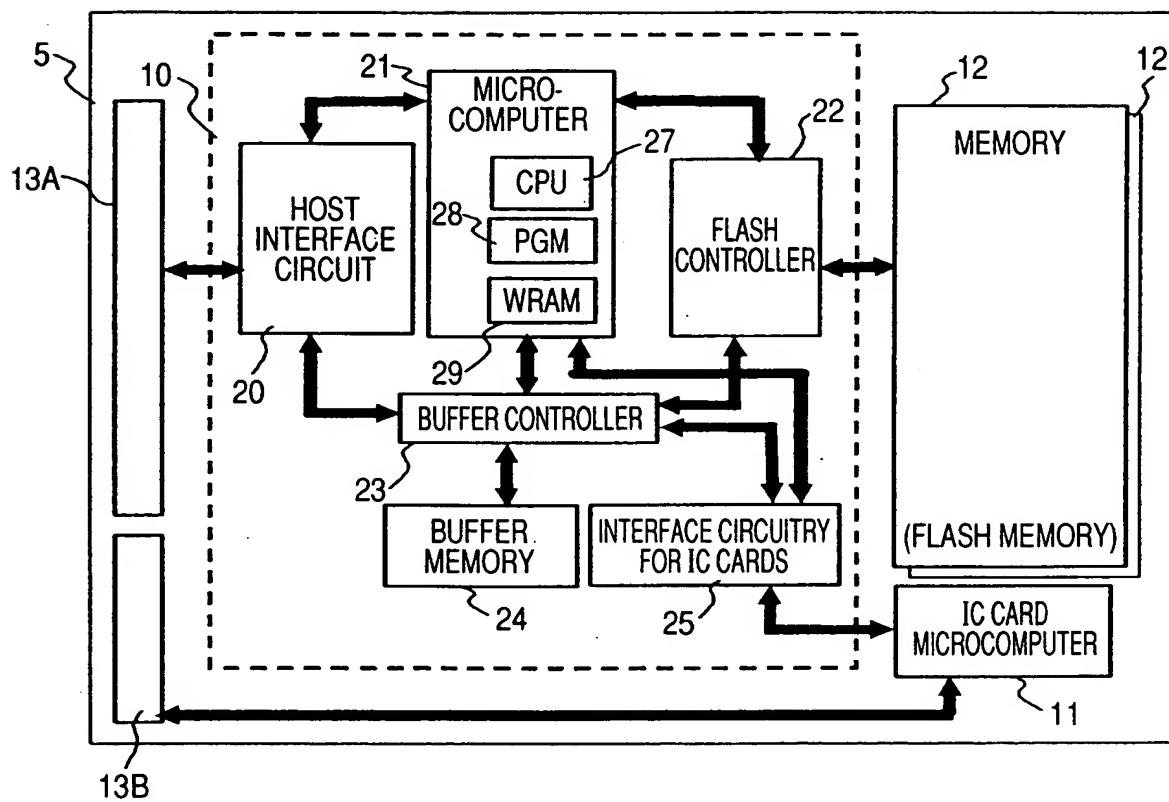


FIG. 11

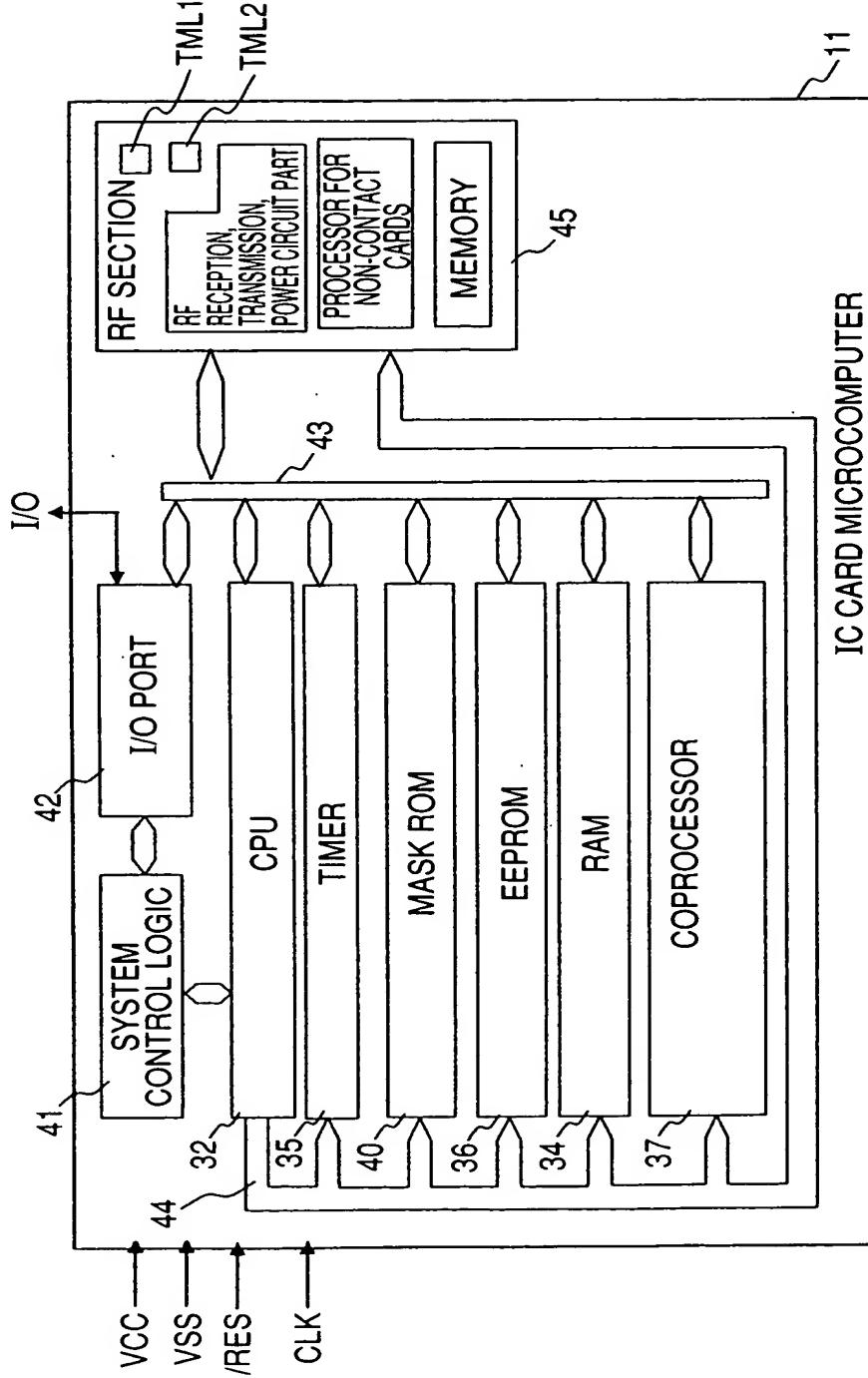


FIG. 12

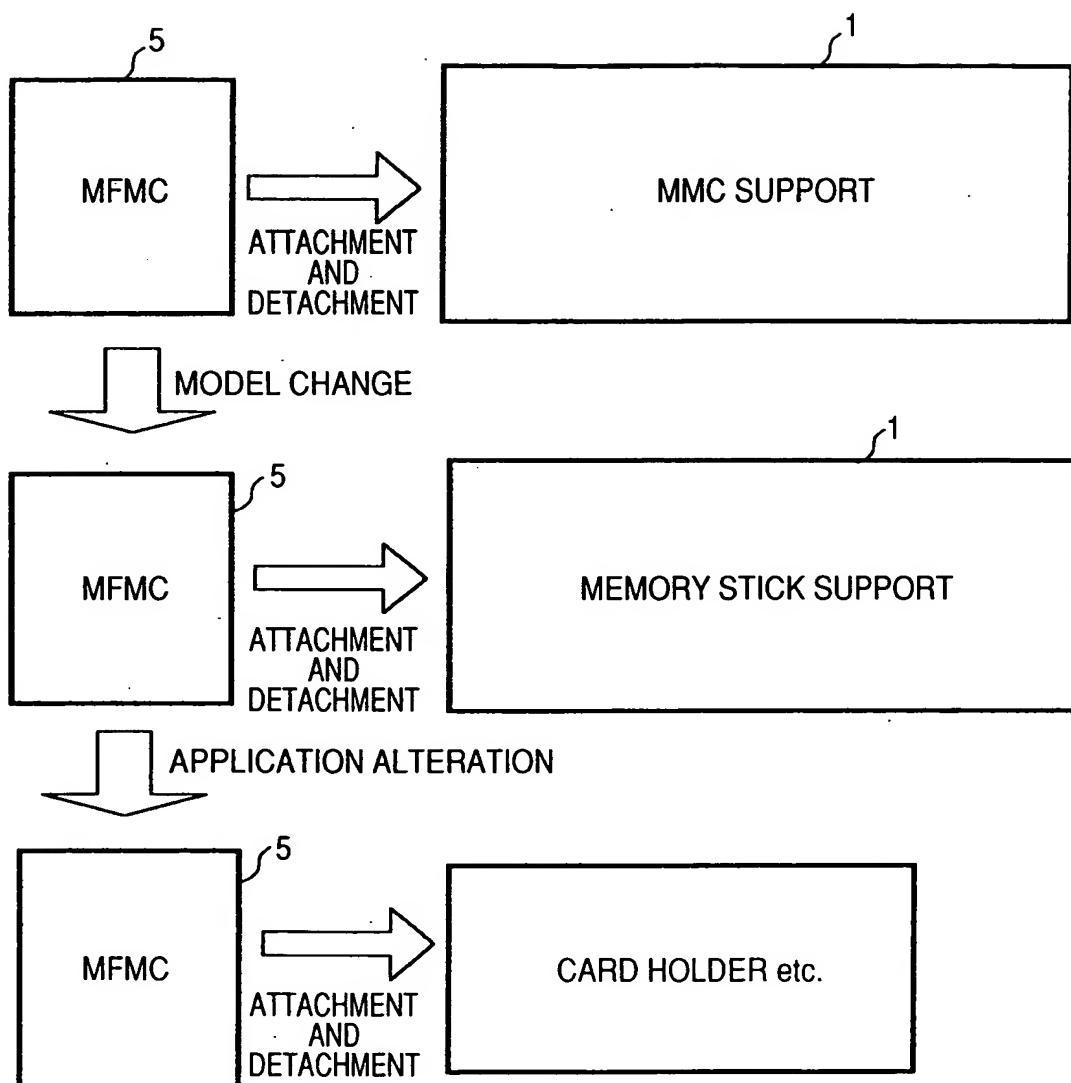


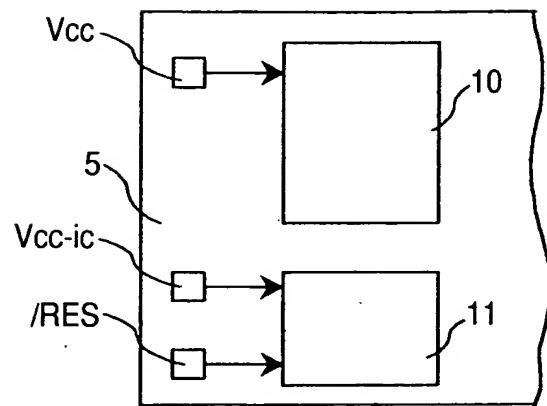
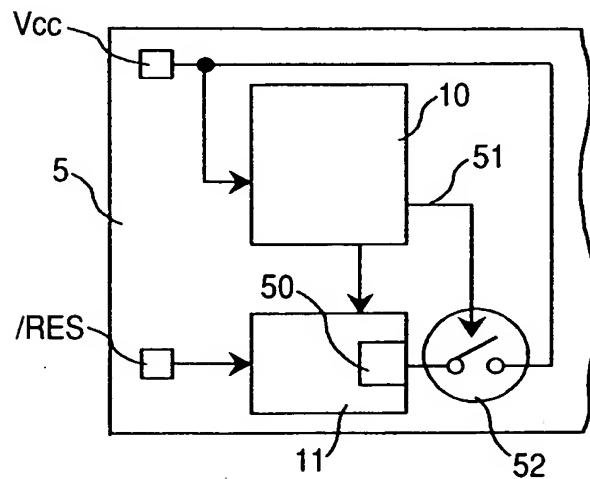
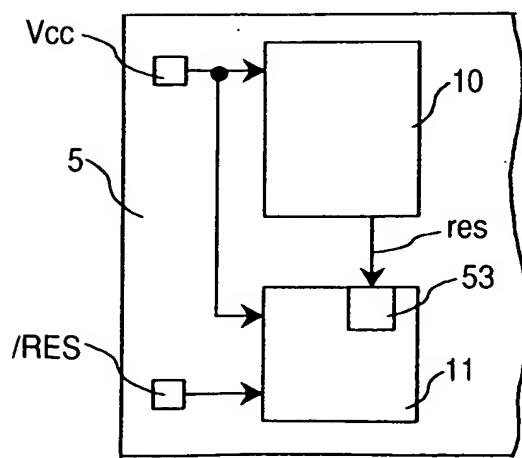
FIG. 13*FIG. 14**FIG. 15*

FIG. 16

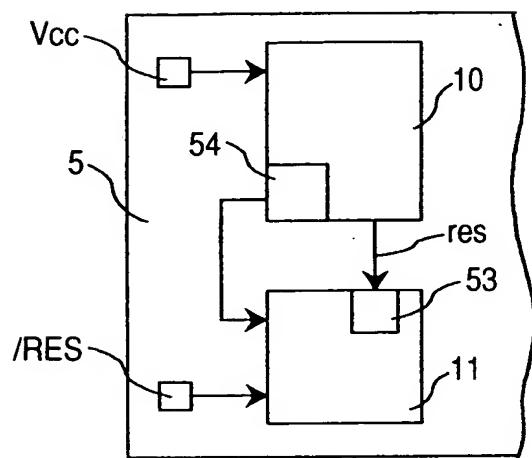


FIG. 17

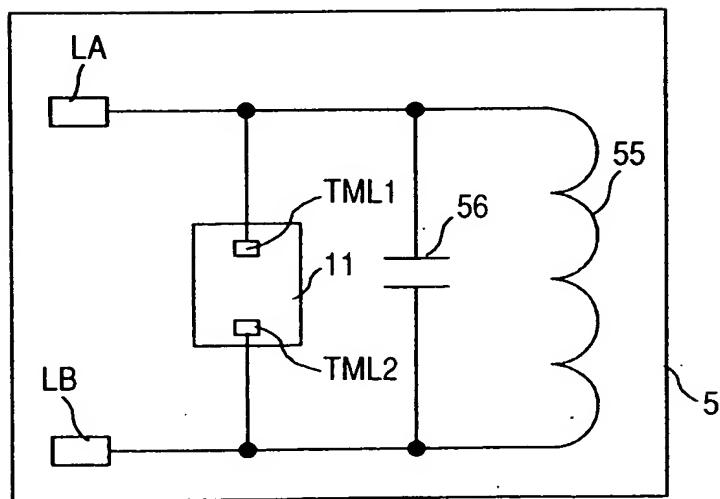


FIG. 18

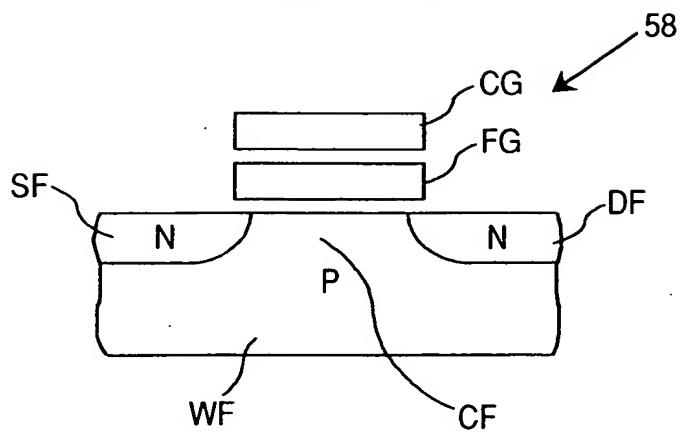


FIG. 19

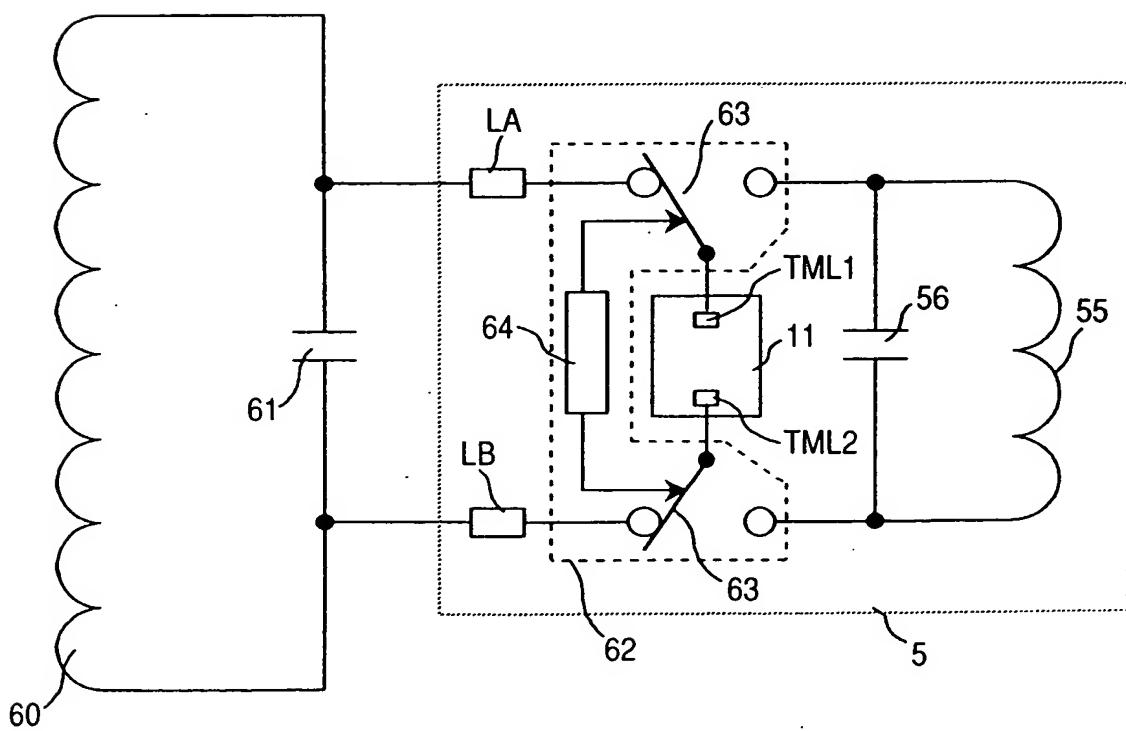


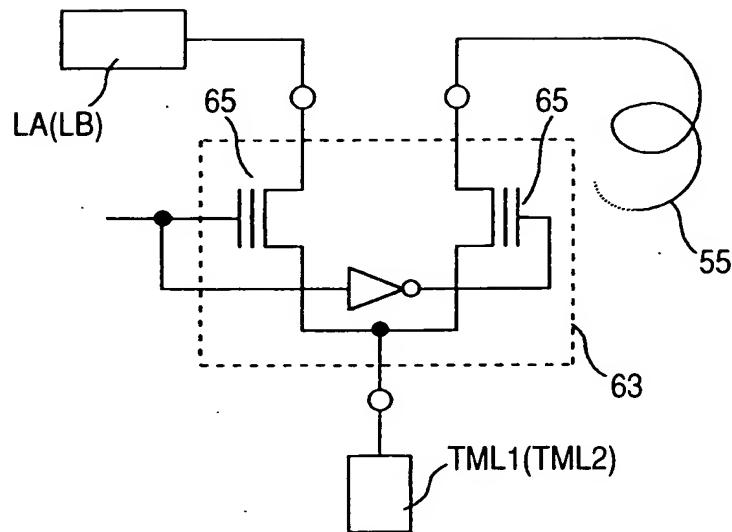
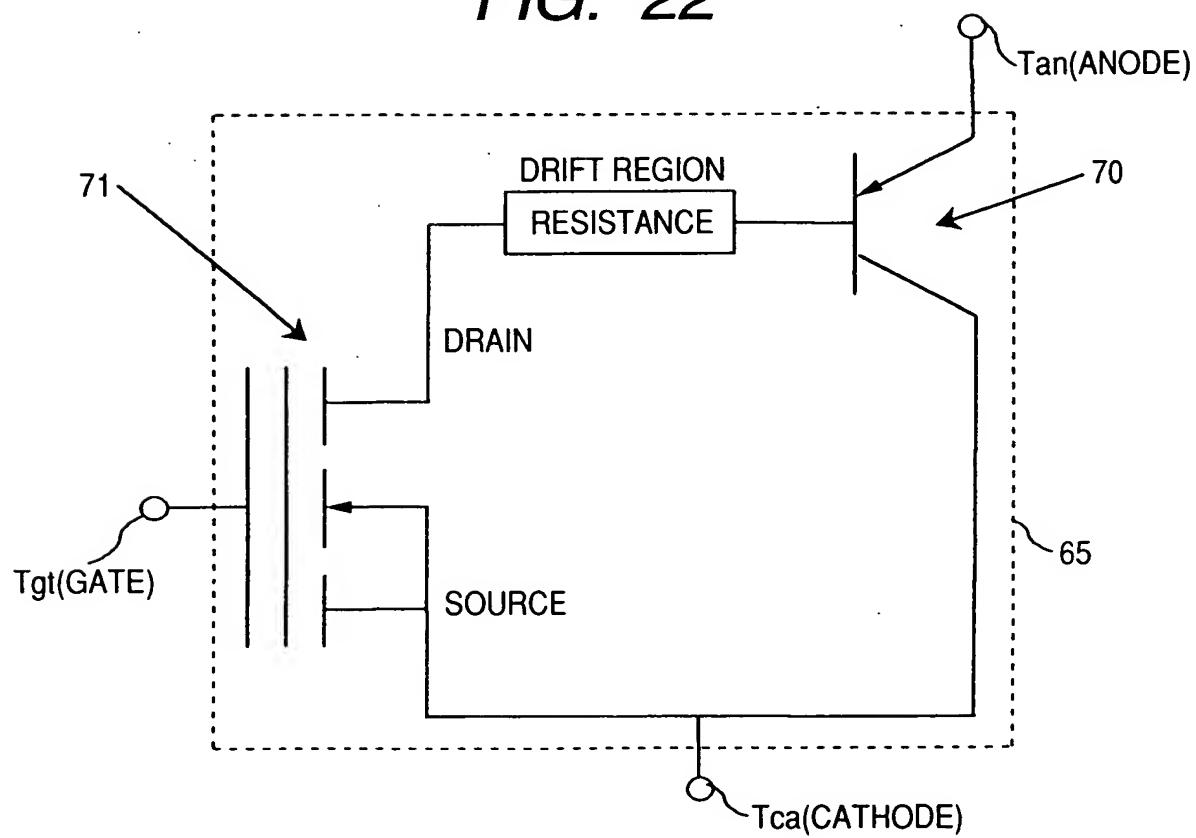
FIG. 20*FIG. 22*

FIG. 21

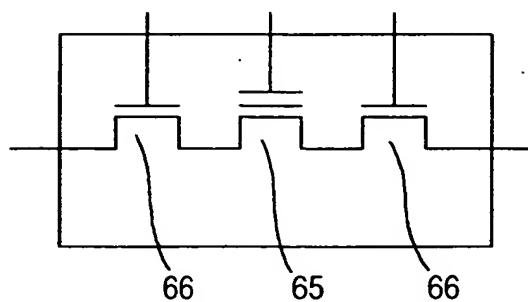


FIG. 24

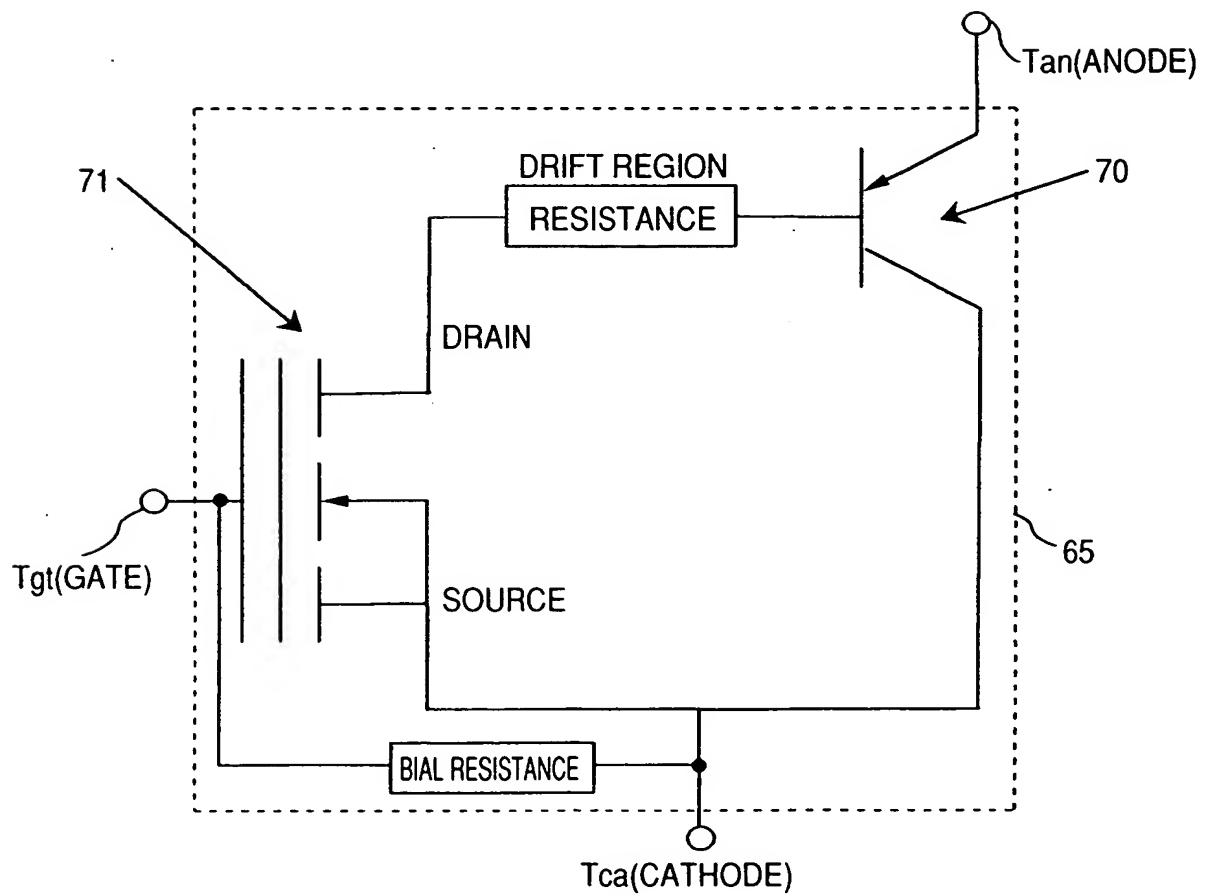


FIG. 23

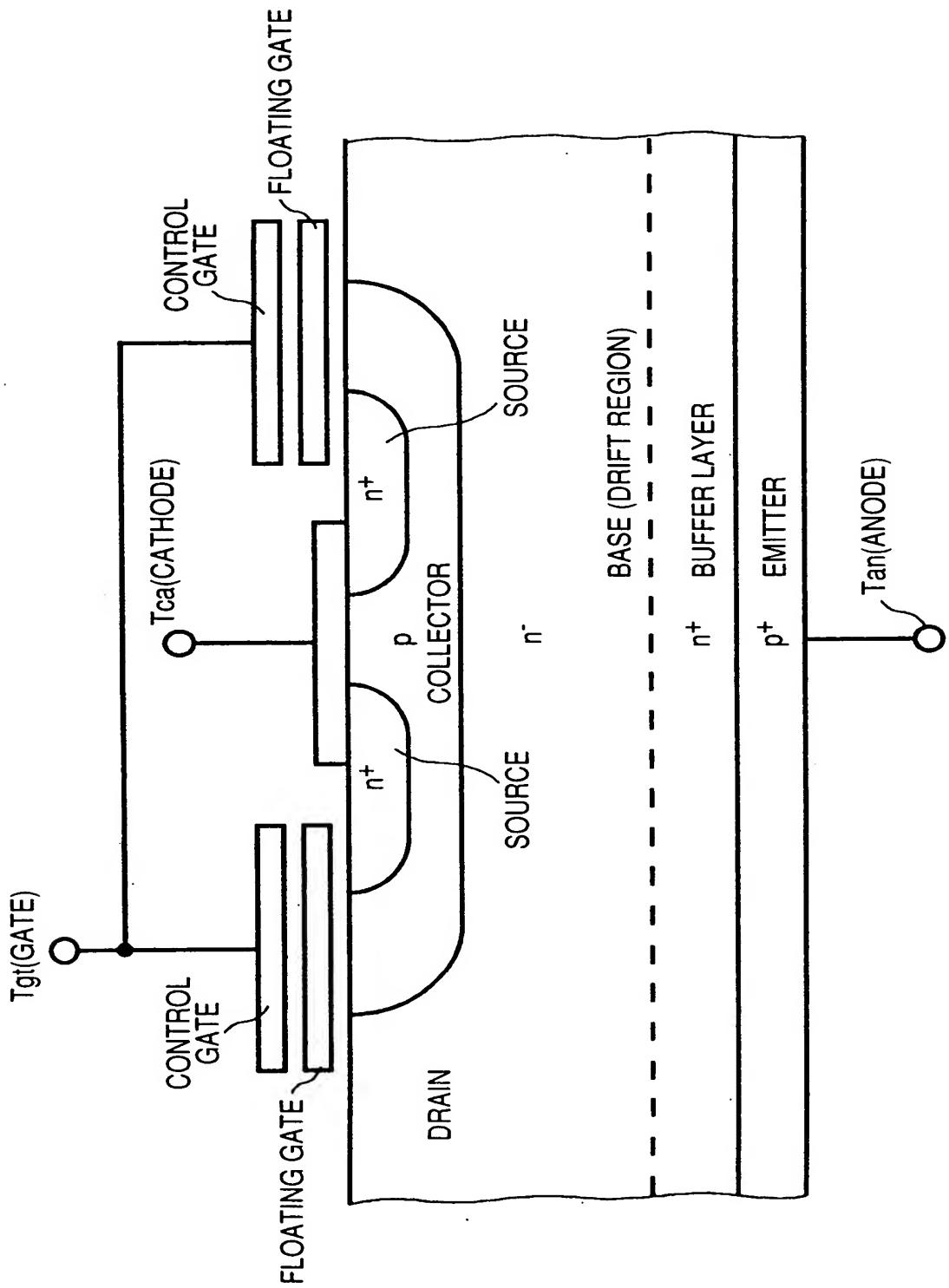


FIG. 25

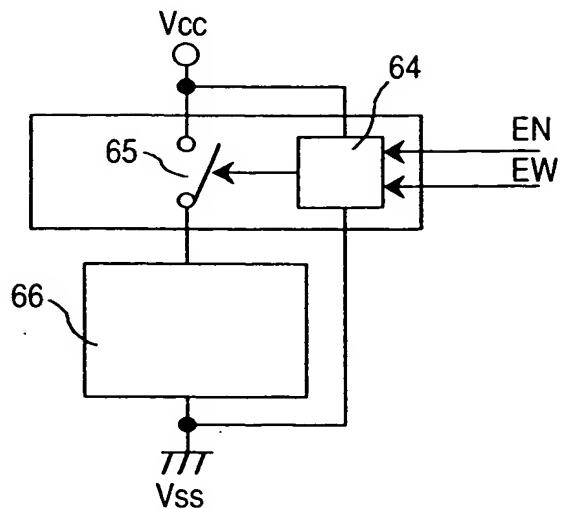


FIG. 26

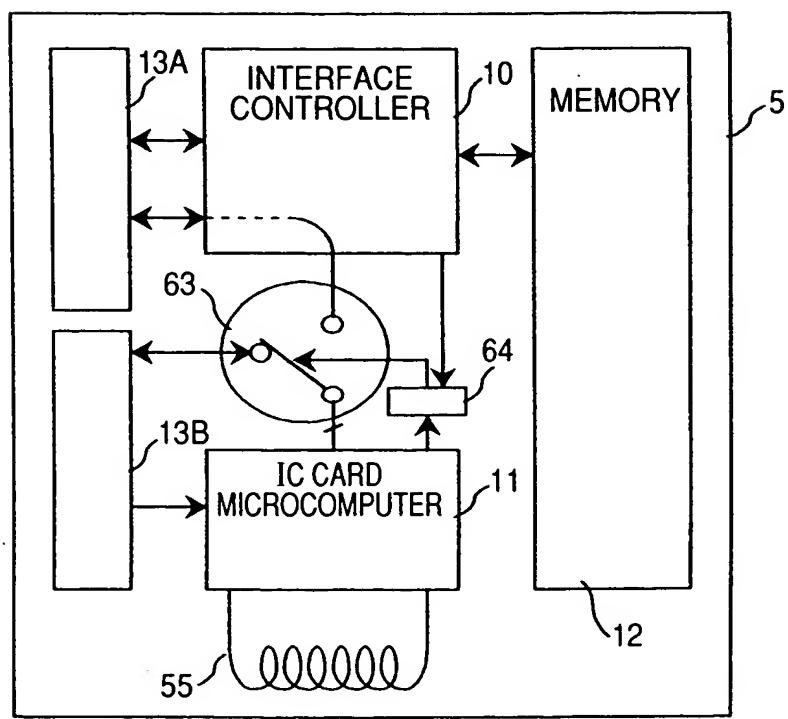


FIG. 27

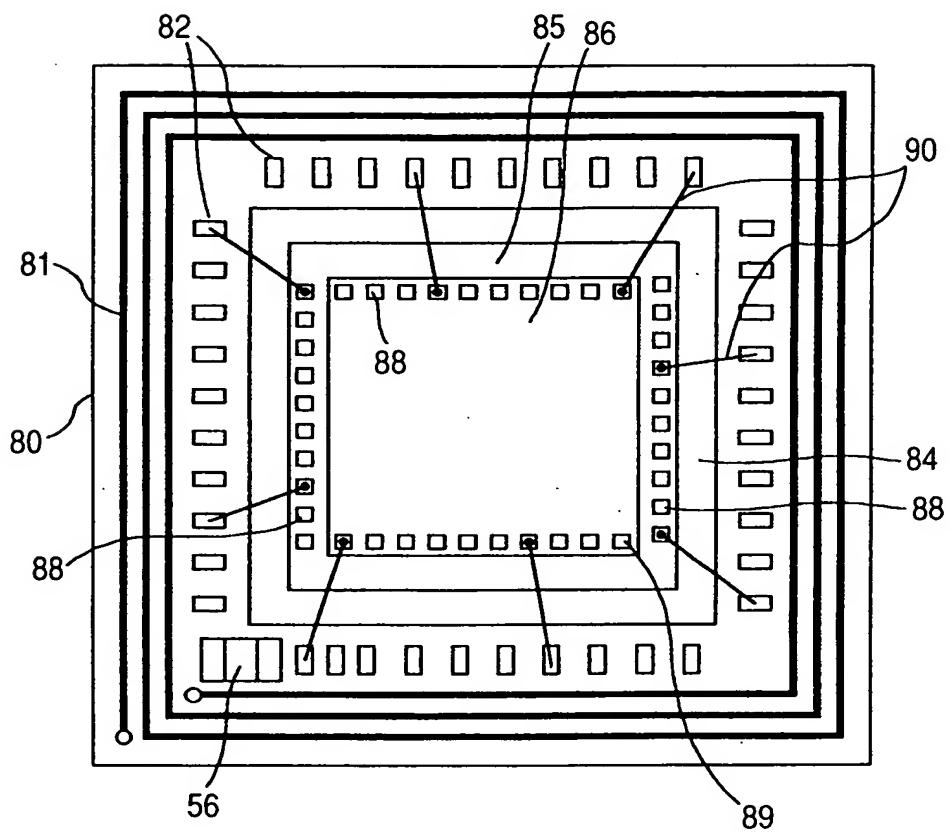


FIG. 28

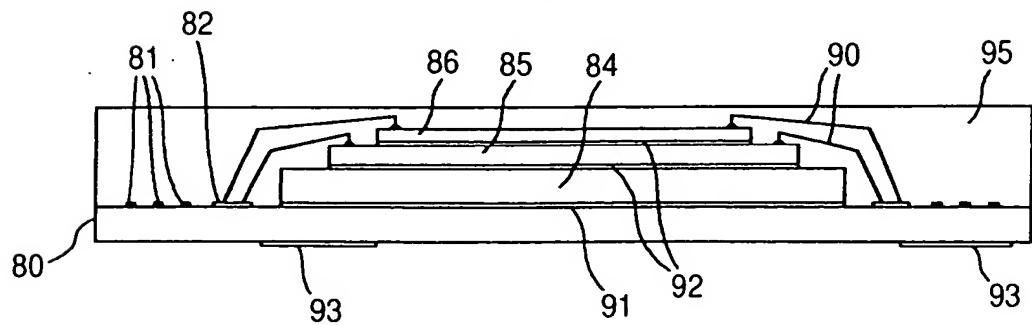


FIG. 29

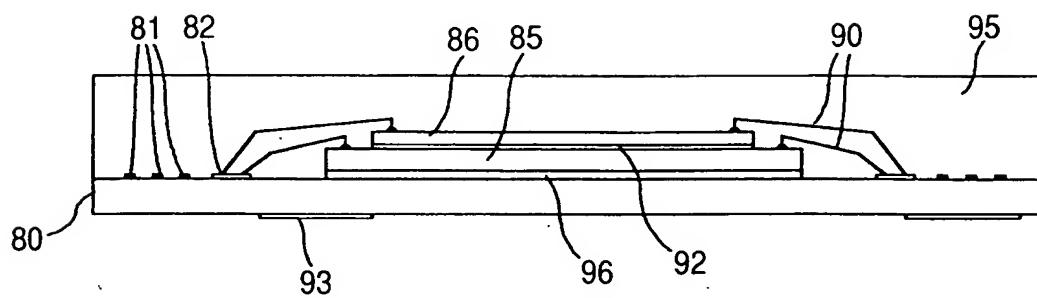


FIG. 31

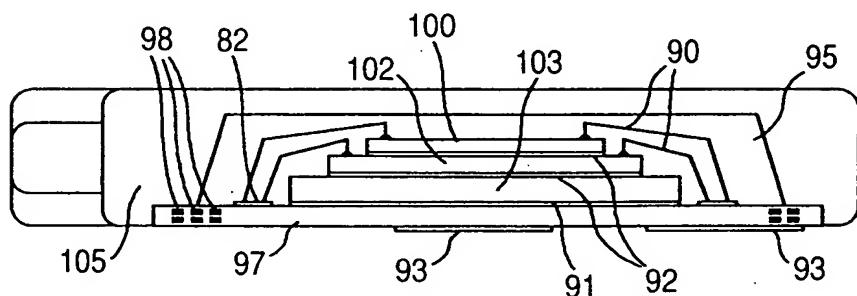


FIG. 32

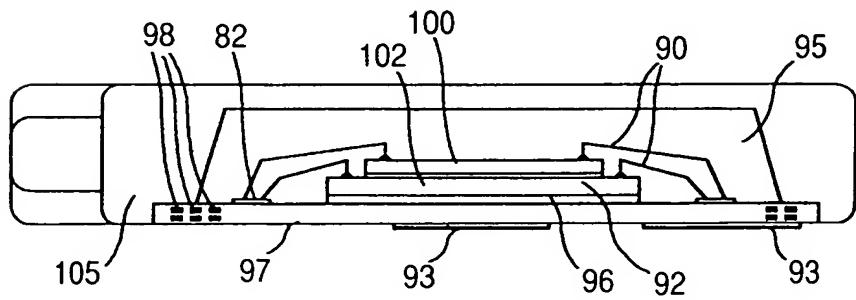


FIG. 30

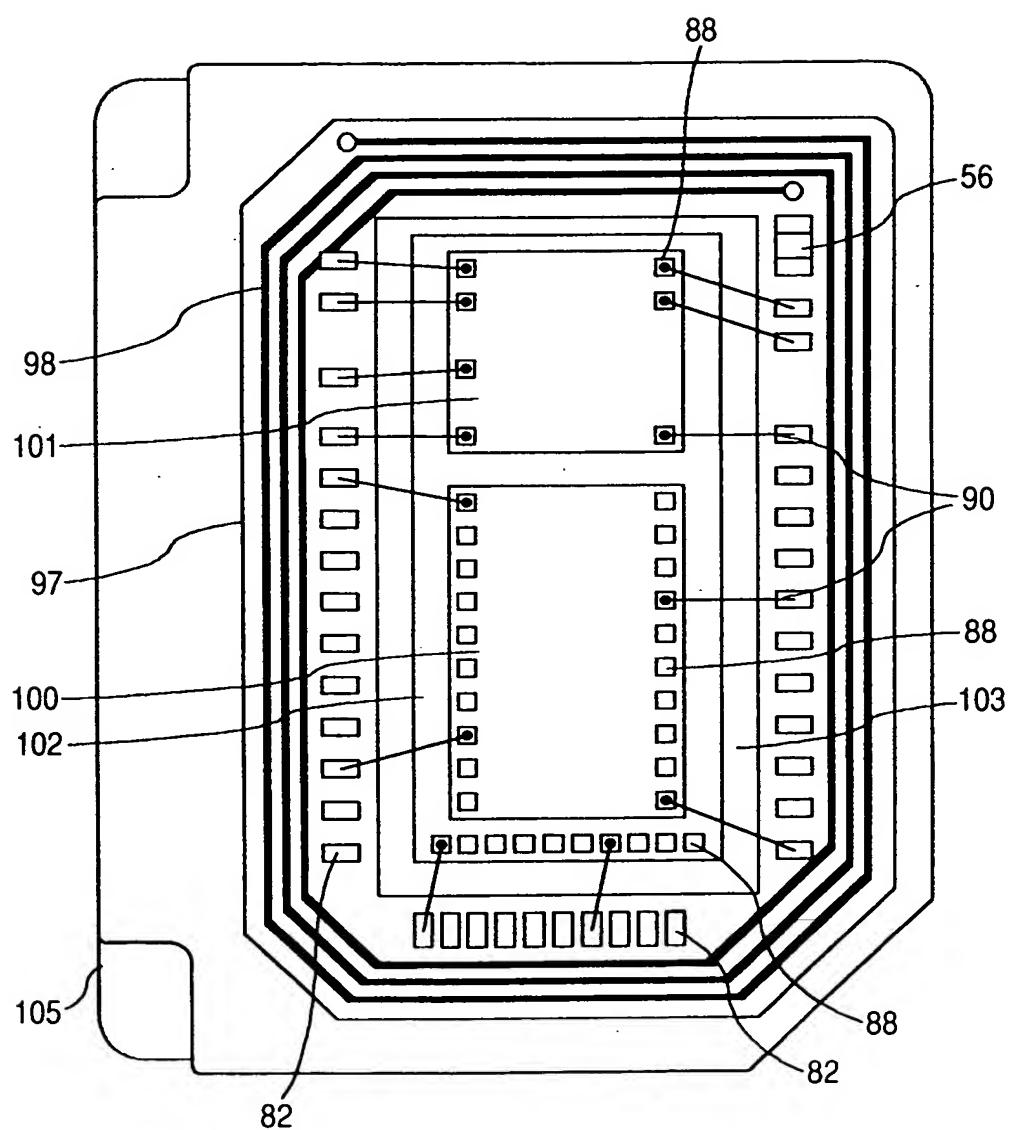


FIG. 33

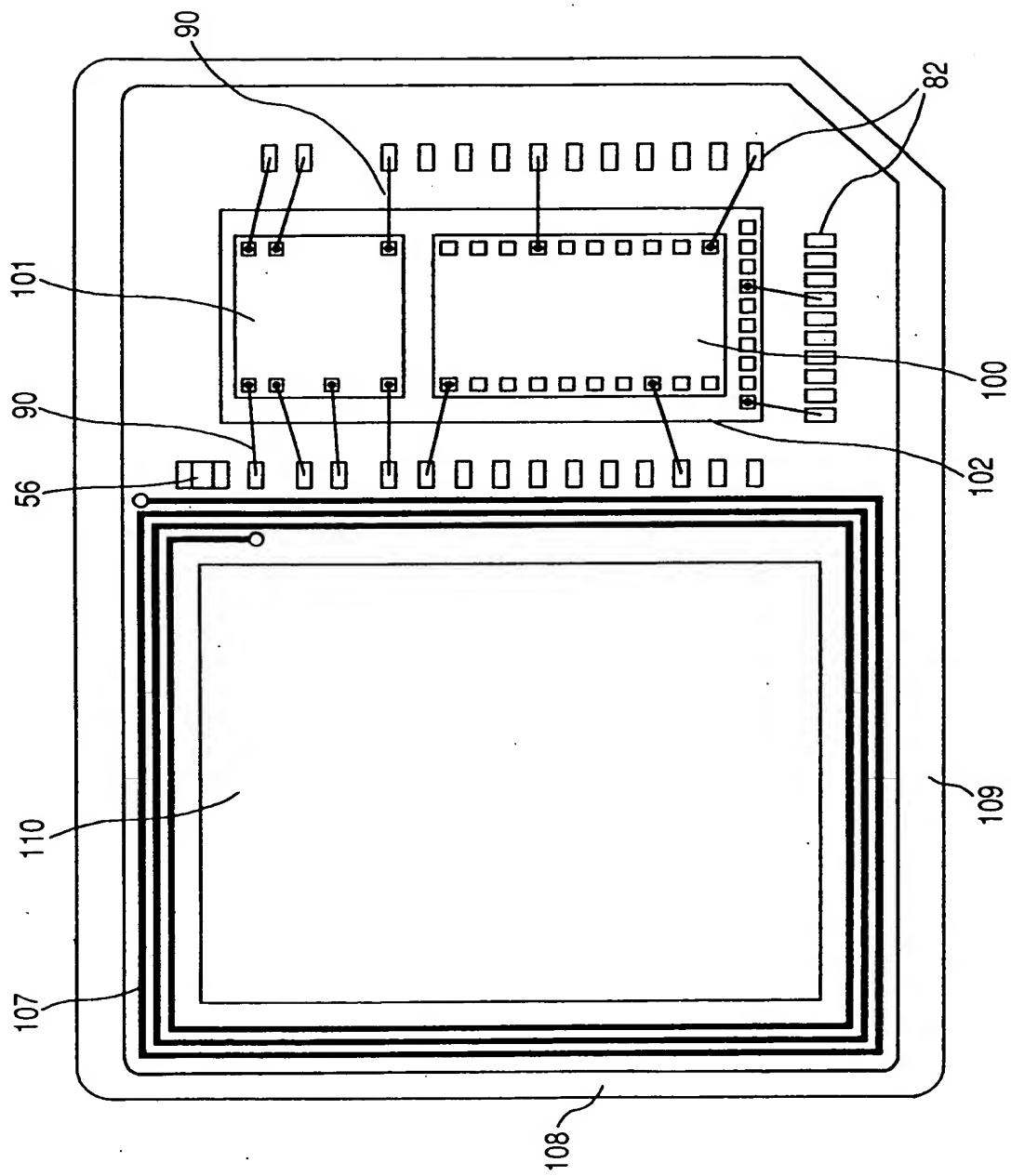


FIG. 34

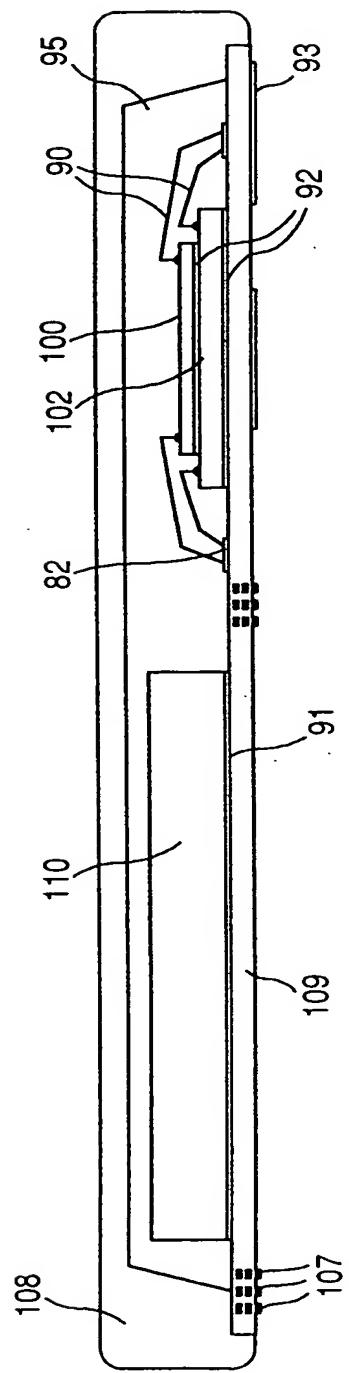


FIG. 35

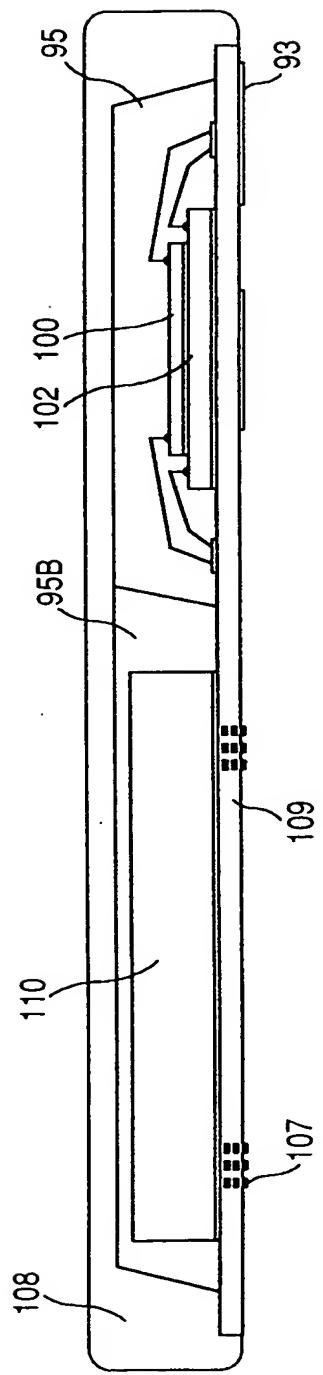


FIG. 36

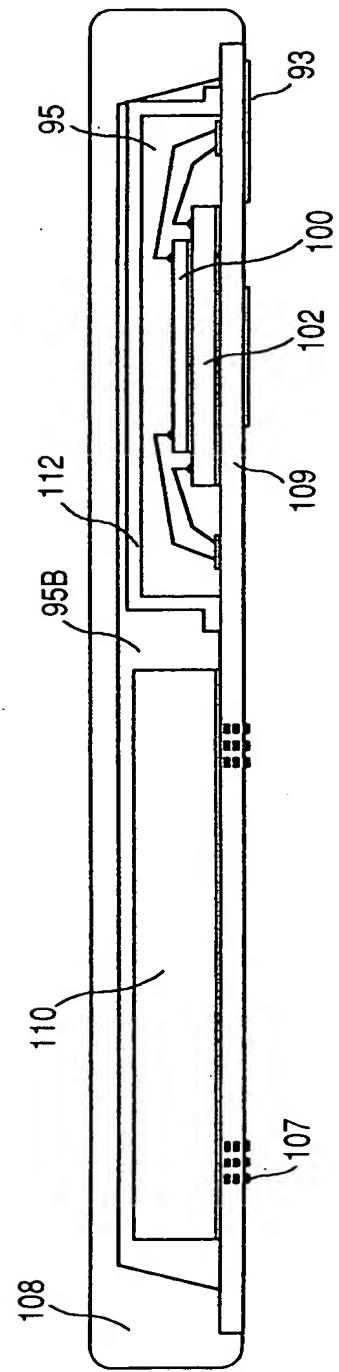


FIG. 37

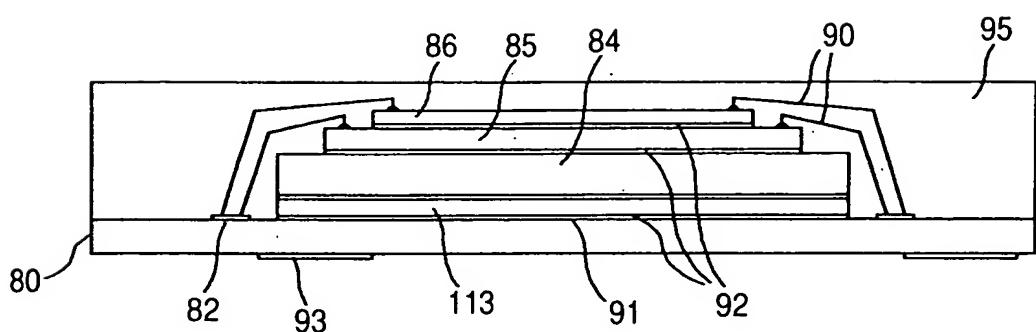


FIG. 38

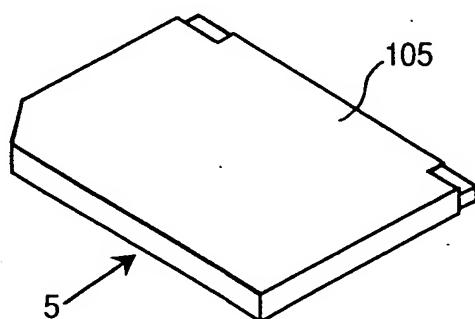


FIG. 39

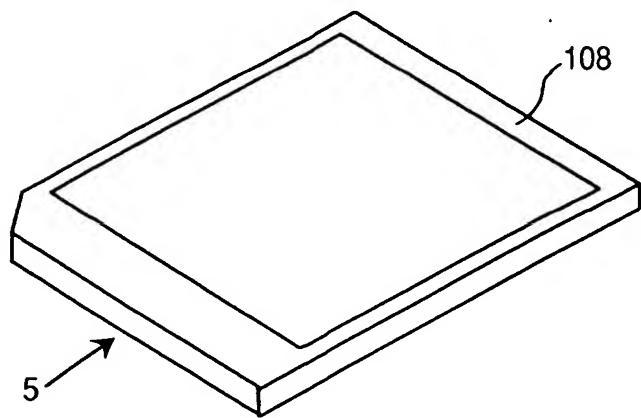


FIG. 40

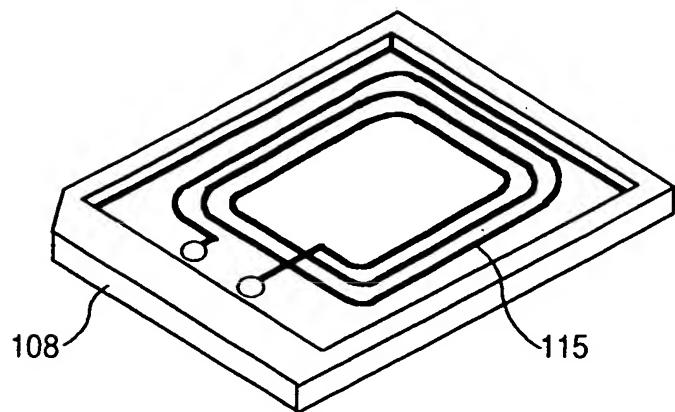


FIG. 41

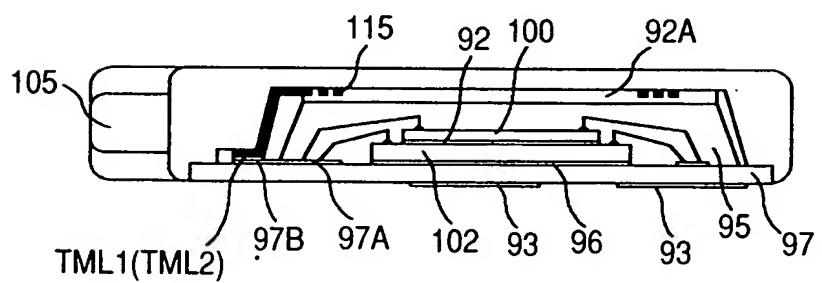


FIG. 42

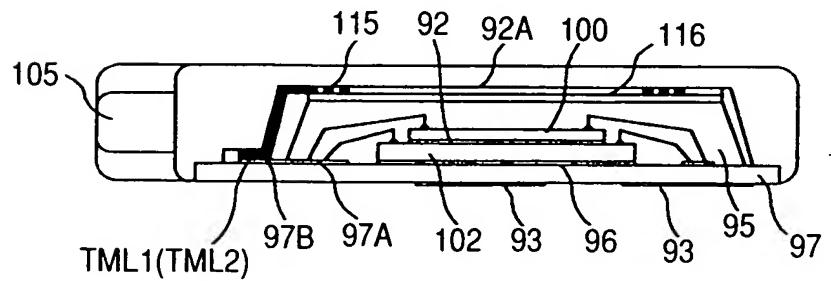


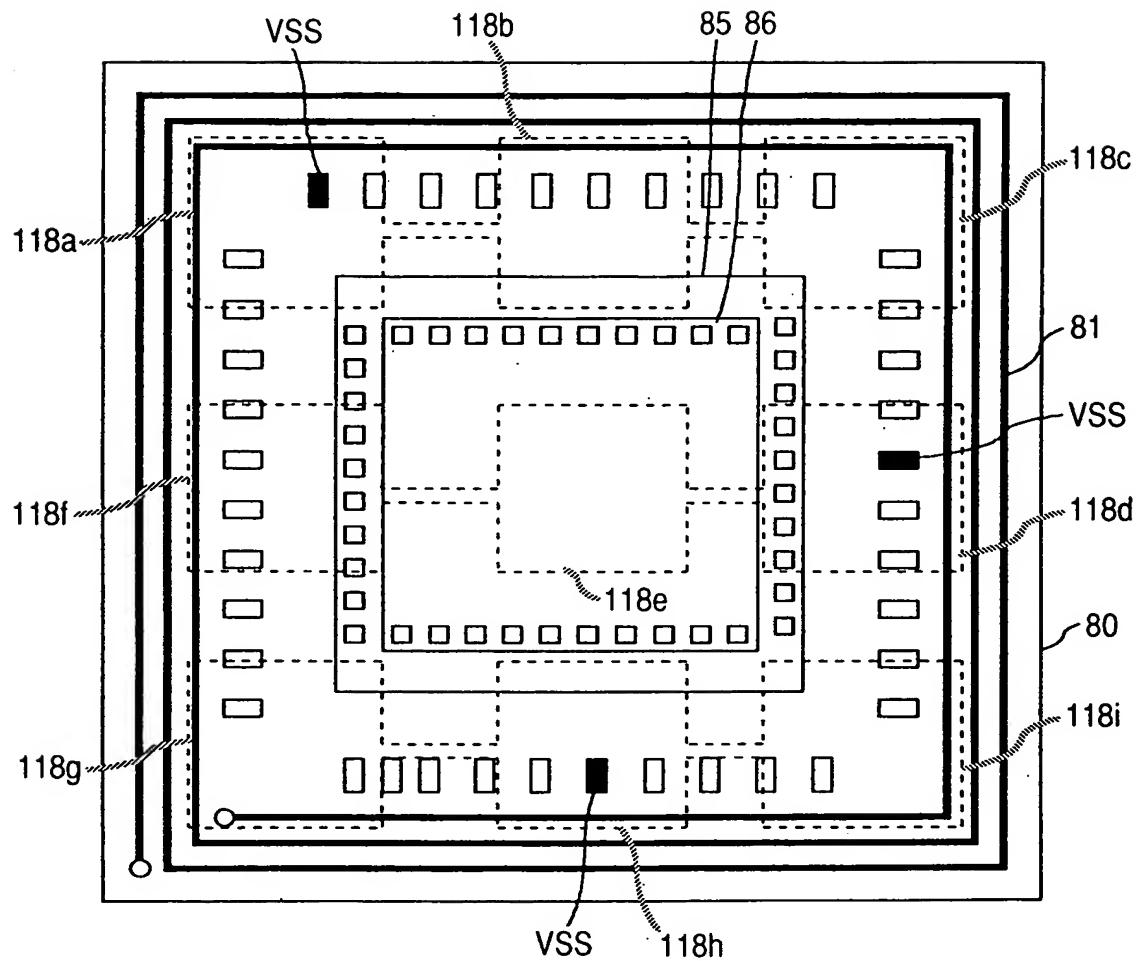
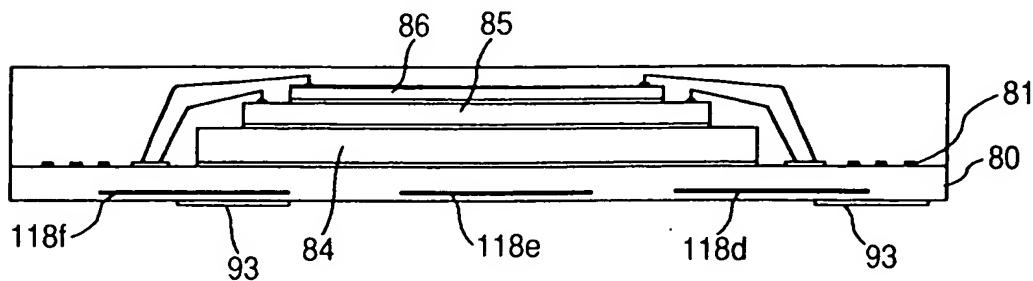
FIG. 43***FIG. 44***

FIG. 45

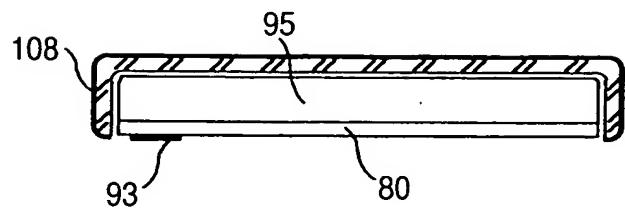


FIG. 46

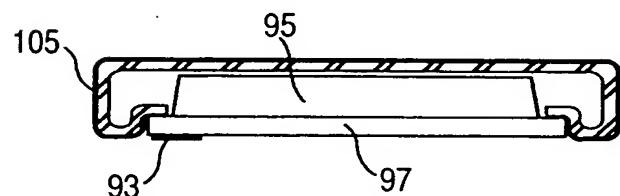


FIG. 47

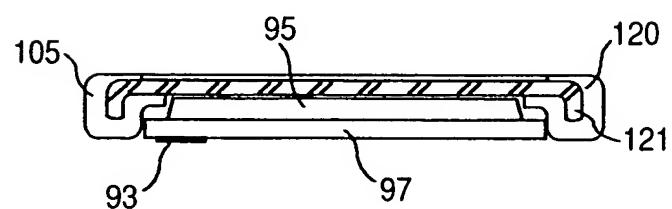


FIG. 48

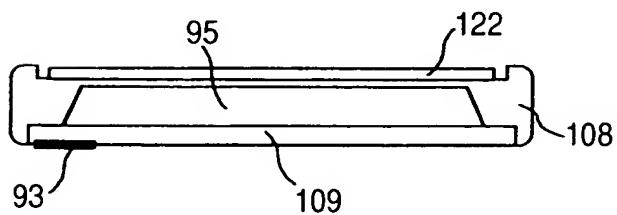


FIG. 49

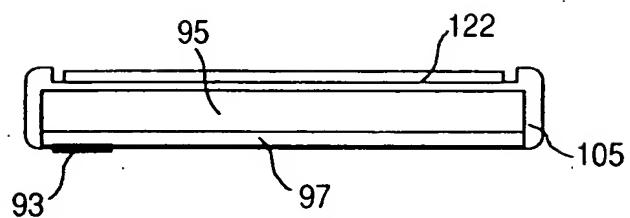


FIG. 50

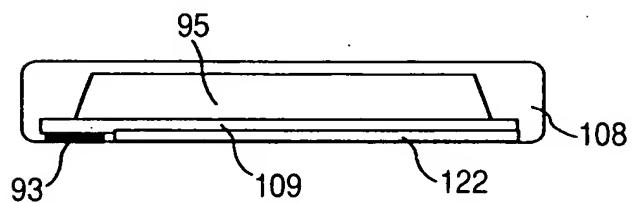


FIG. 51

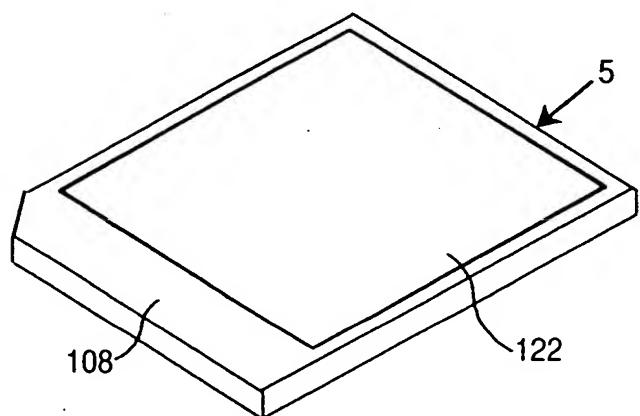


FIG. 52

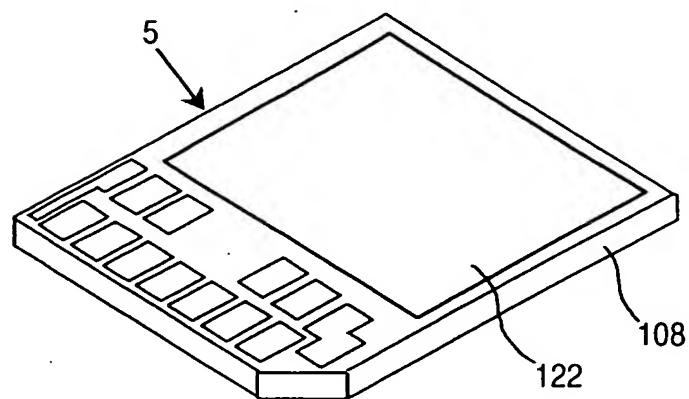


FIG. 53

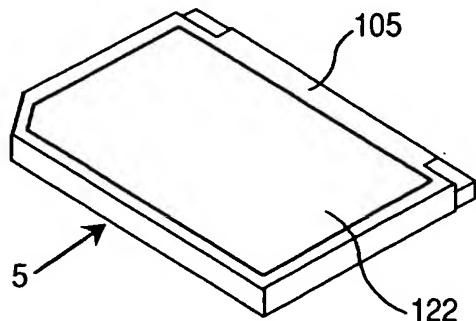


FIG. 54

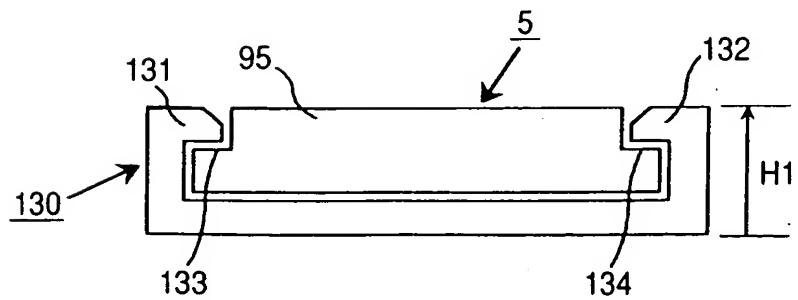


FIG. 55

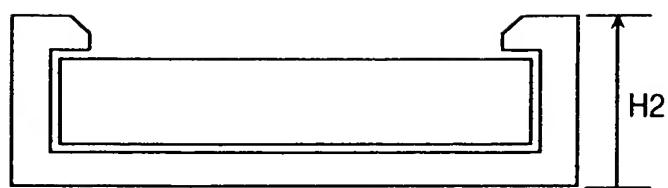


FIG. 57

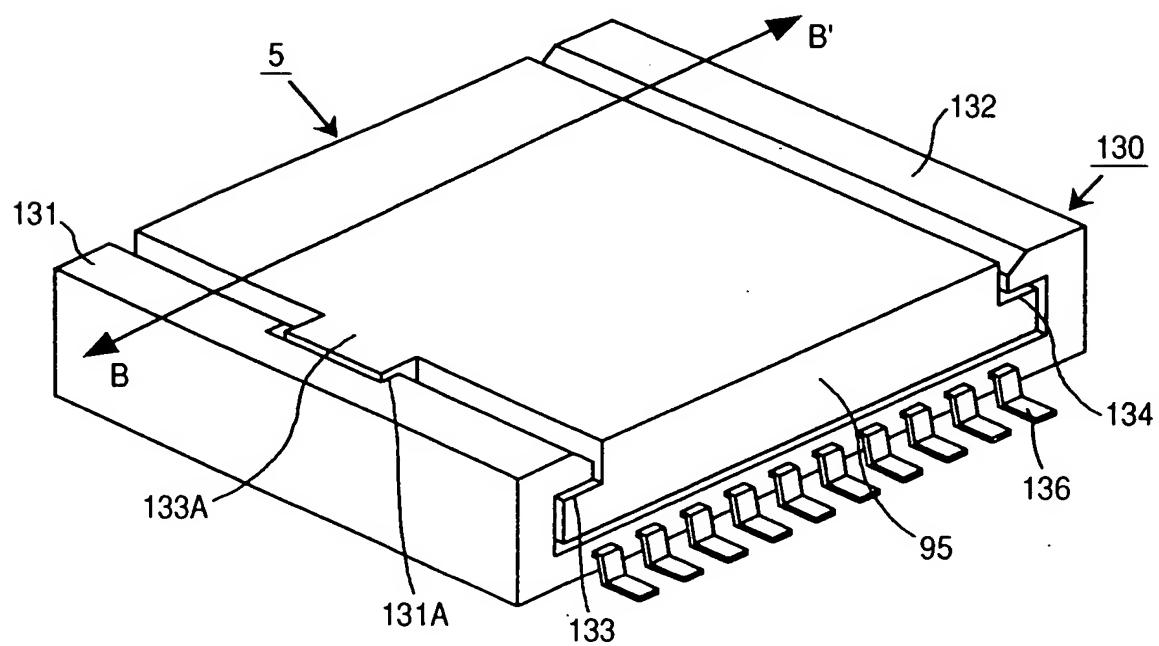


FIG. 56

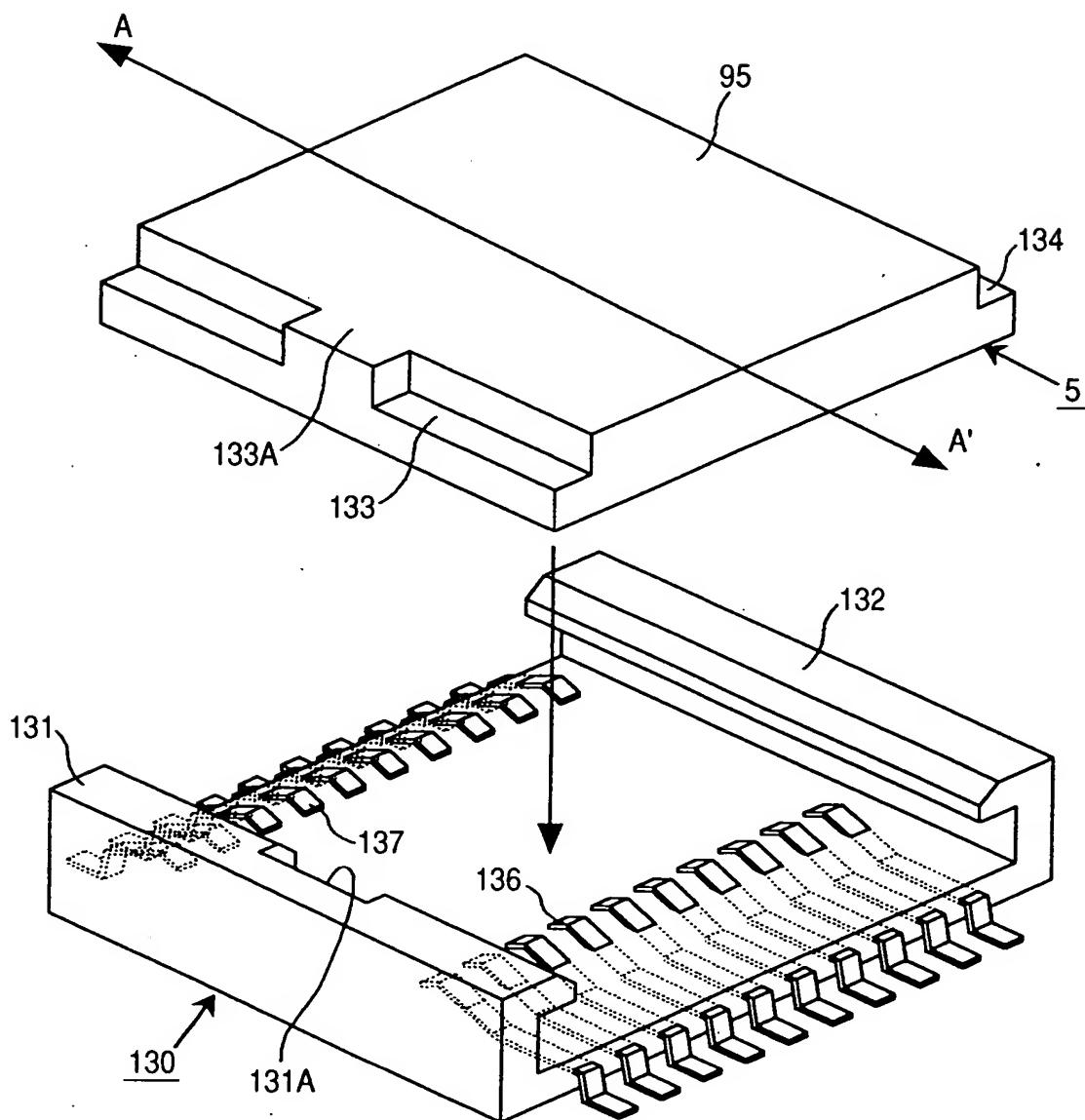


FIG. 58

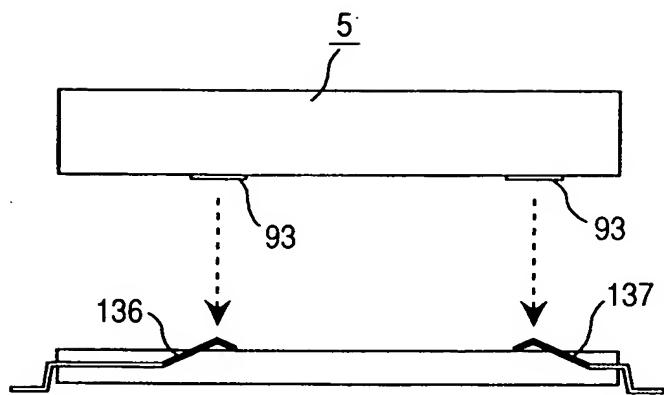


FIG. 59

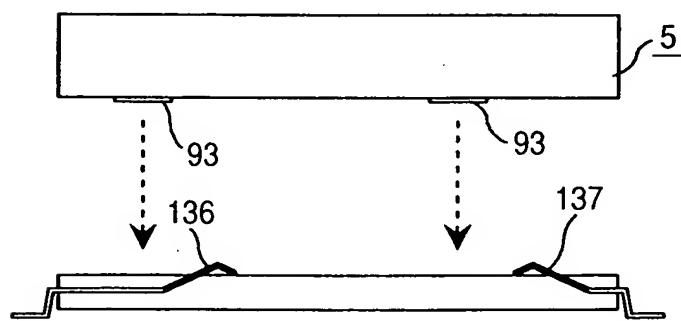


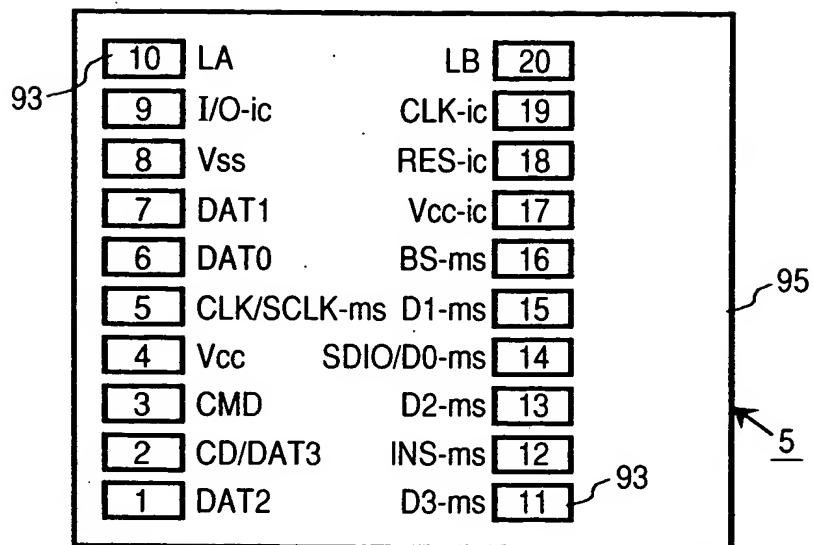
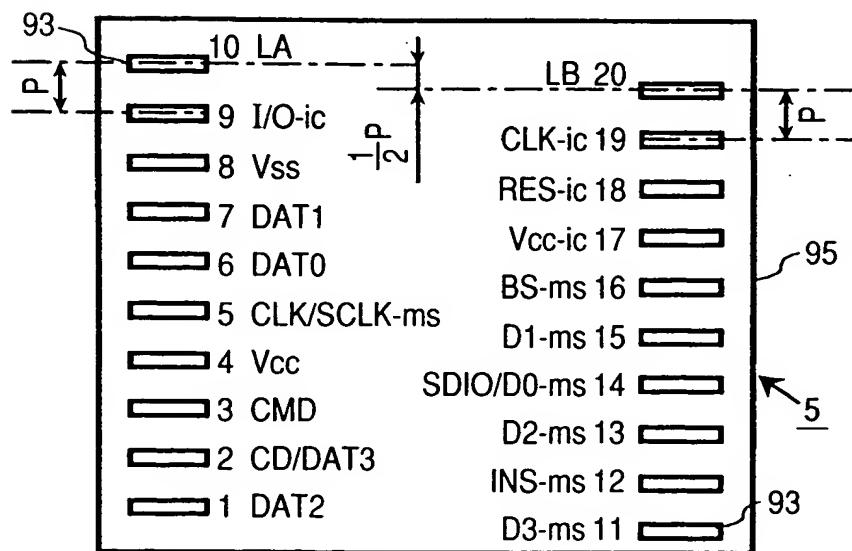
FIG. 60**FIG. 61**

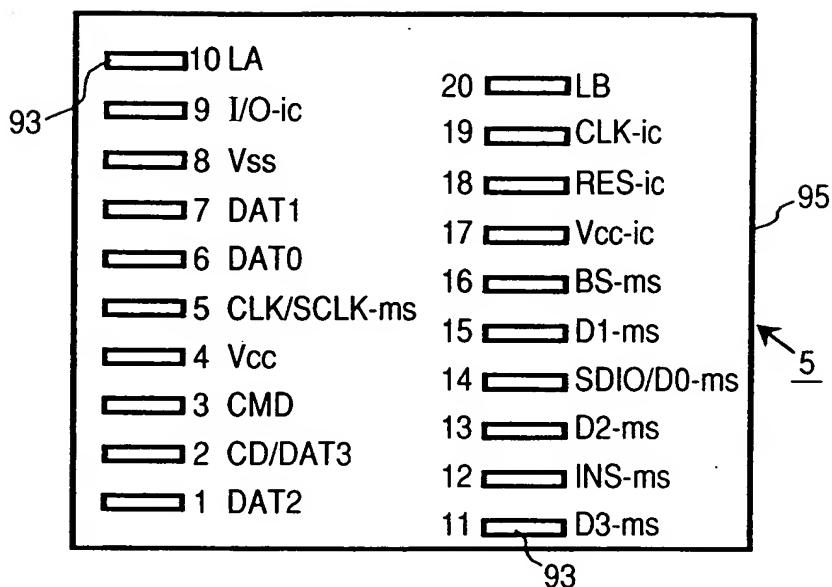
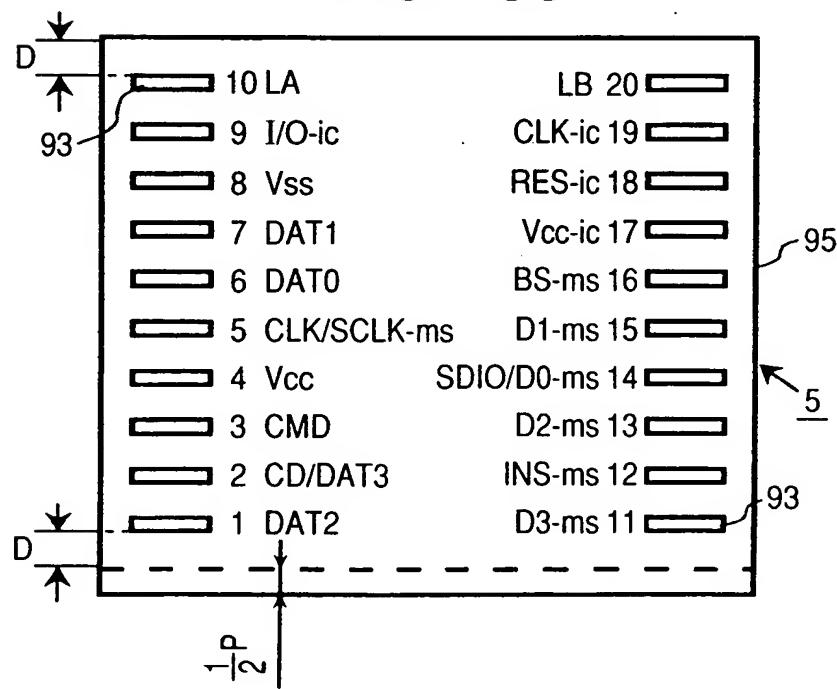
FIG. 62**FIG. 63**

FIG. 64

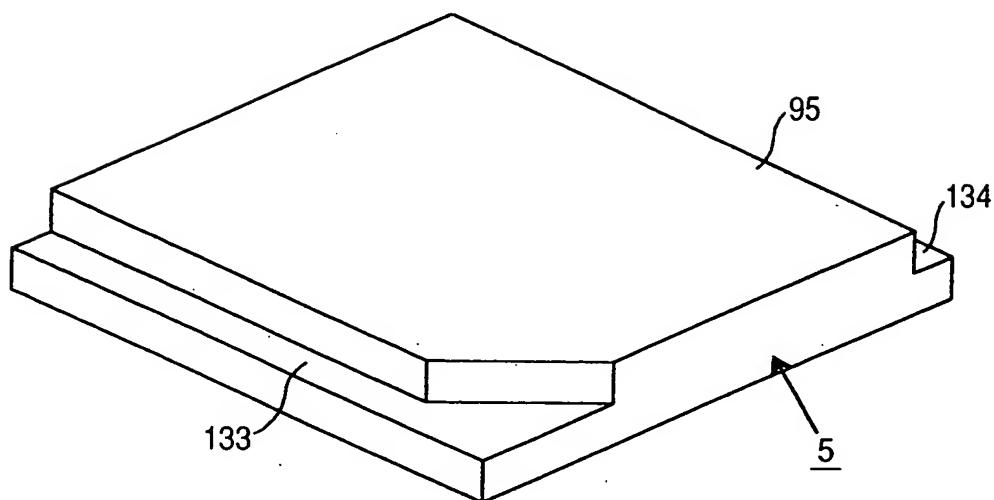


FIG. 65

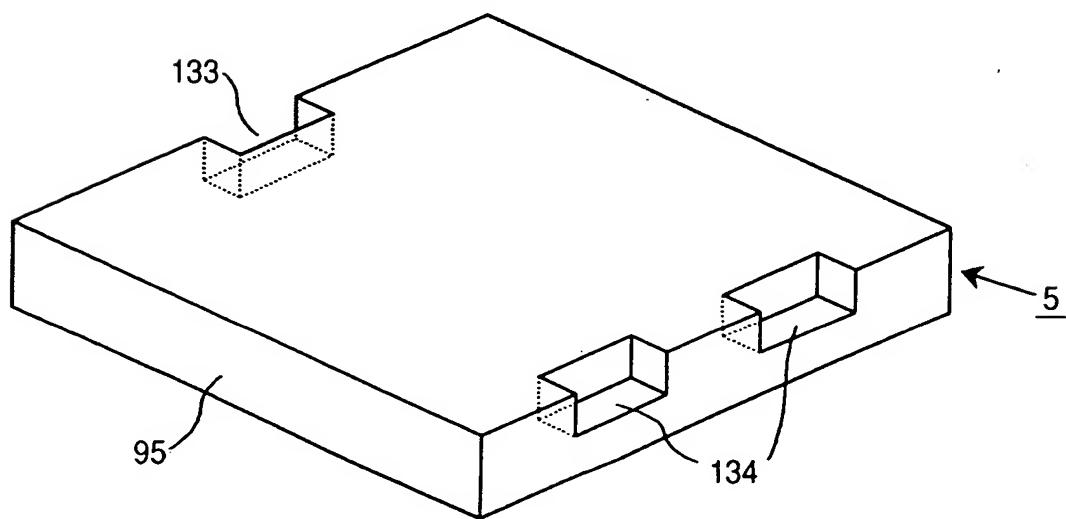


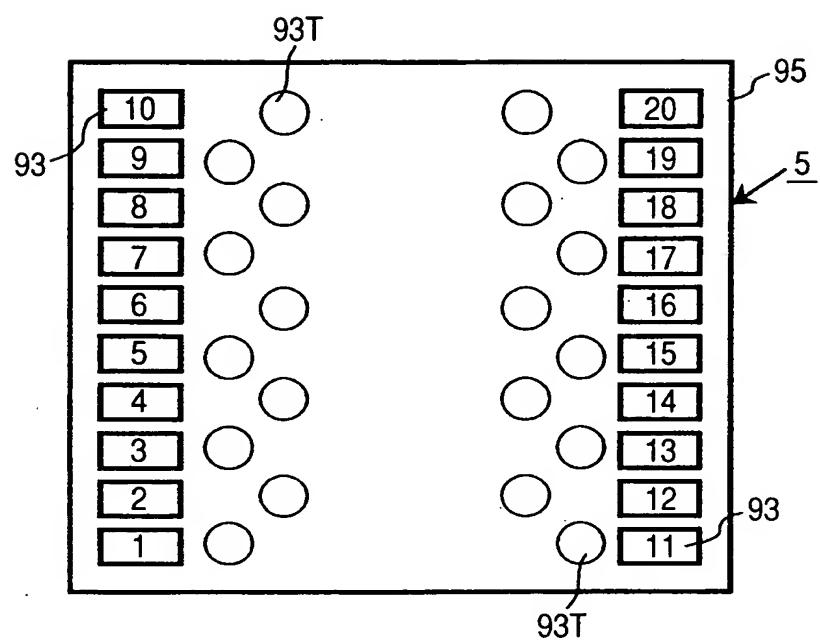
FIG. 66

FIG. 67

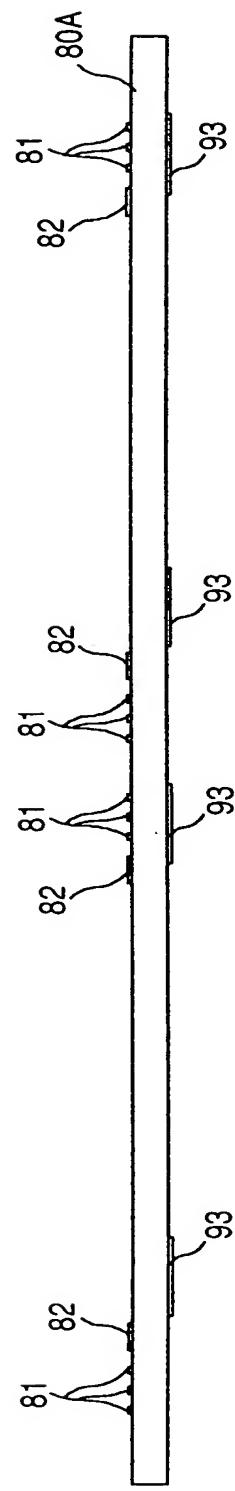


FIG. 68

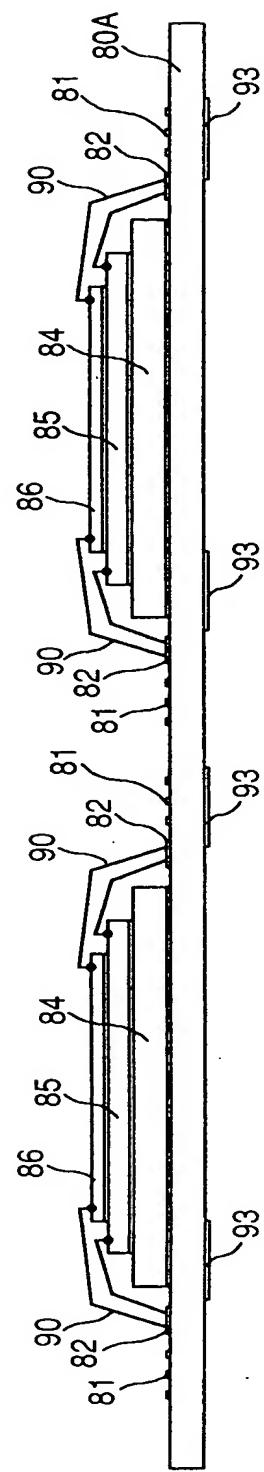


FIG. 69

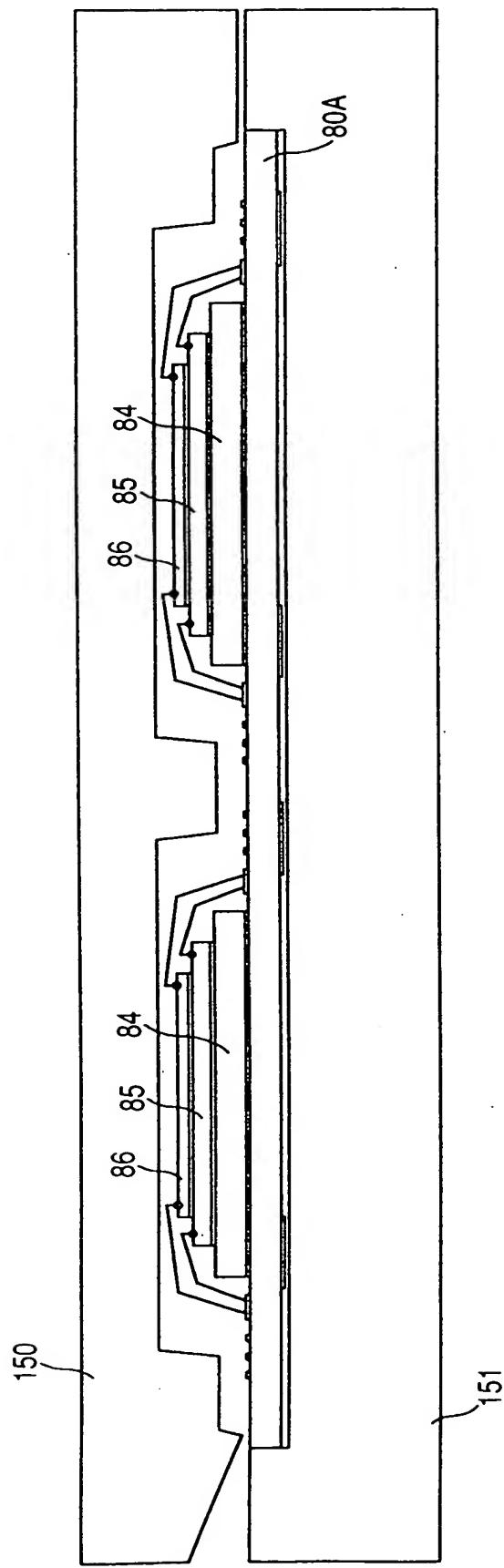


FIG. 70

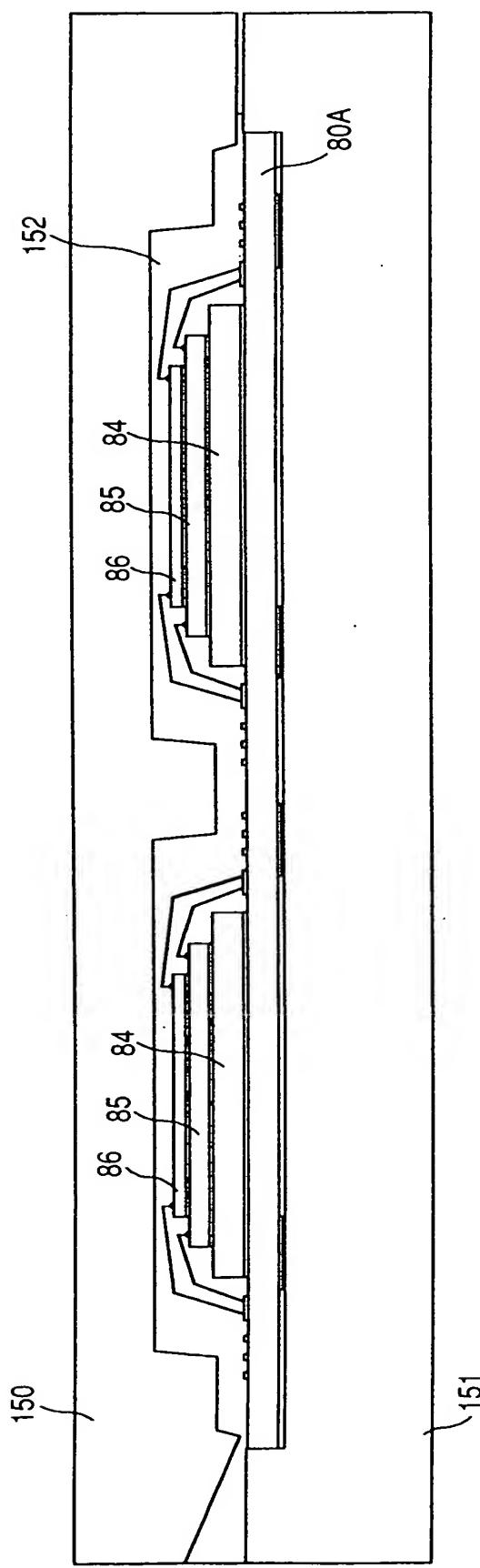


FIG. 71

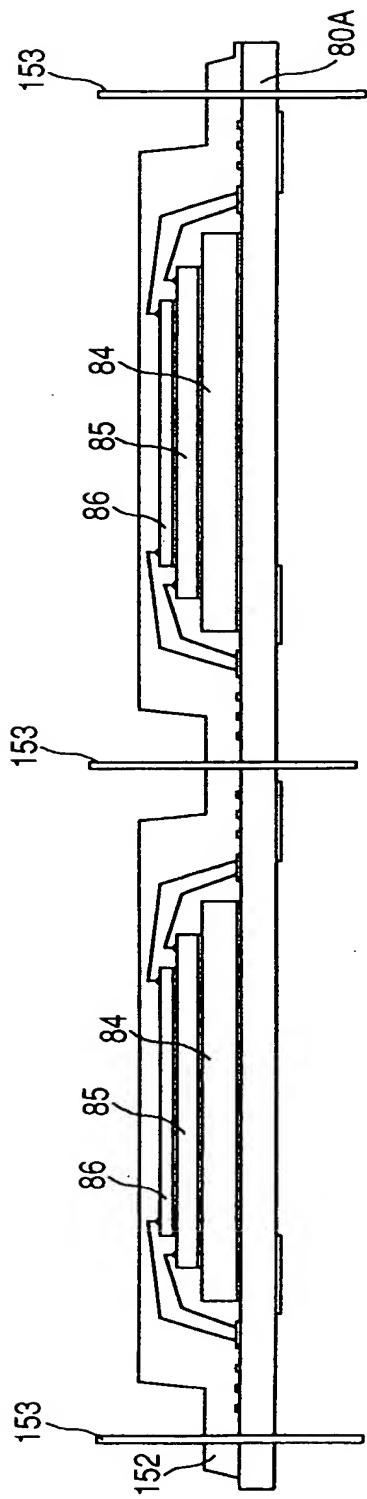


FIG. 72

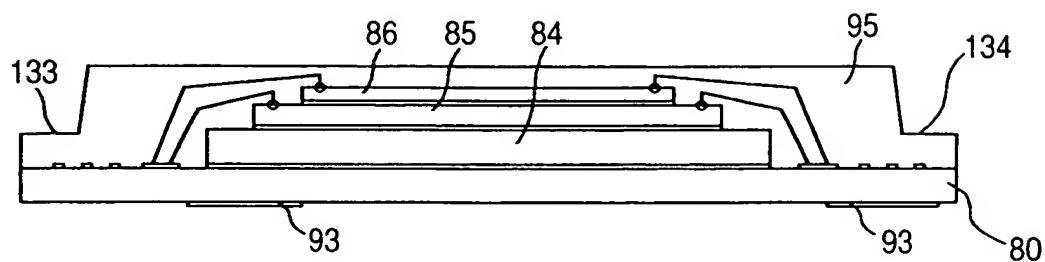


FIG. 73

